

# Compilation (#6a) : SSA ENSLOnly

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Master 1, ENS de Lyon et Dpt Info, Lyon1

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- 1 SSA Control Flow Graph
- 2 LAB: CFG + SSA
- 3 Exercises

# Credits

Source <http://homepages.dcc.ufmg.br/~fernando/classes/dcc888/ementa/slides/StaticSingleAssignment.pdf>

- The SSA book (collective)
- Modern Compiler Implementation in C/Java/ML (Andrew Appel)
- Fernando Magno Quintao Pereira's course  
<https://www.youtube.com/user/pronesto/videos>
- Adrian Sampson's course  
<https://www.cs.cornell.edu/courses/cs6120/2020fa/>

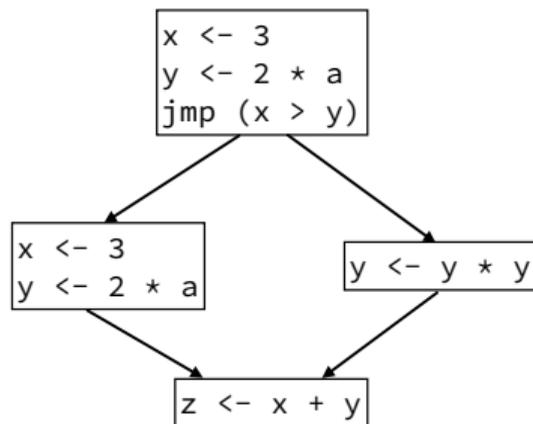
# Motivation: It's all about information

Compilers alternate between two tasks:

1. *computing* some information (invariants) of the program
2. *using* this information to justify some program transformations

Dataflow analyses associate facts to every program point:

- \* a fact is *associated* to a *definition-site* of a variable
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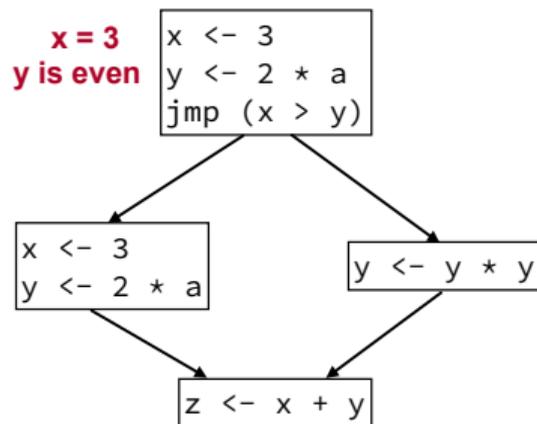
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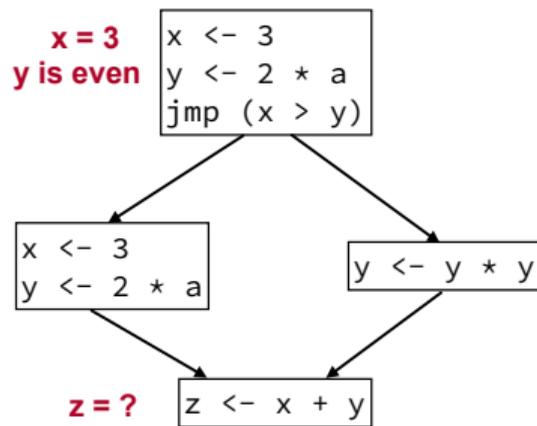
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What do we know about x and y?

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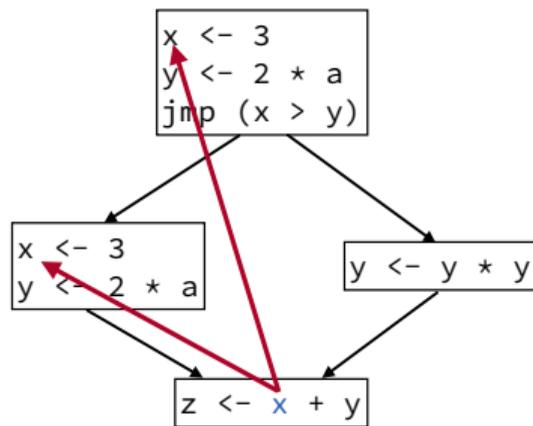
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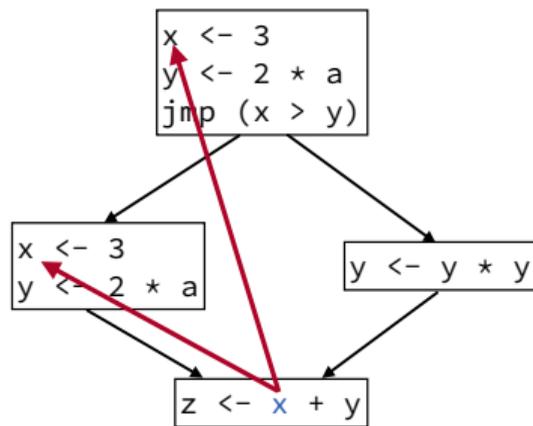
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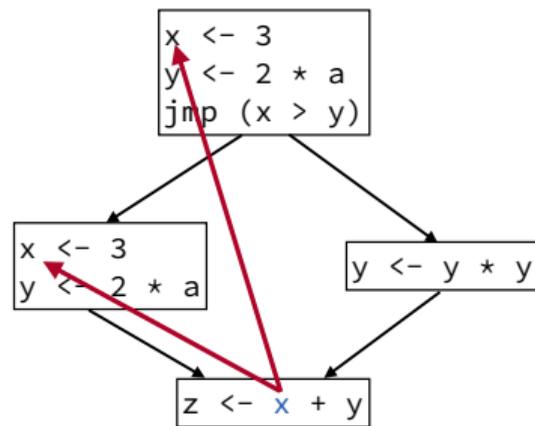
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We want to enforce an invariant by construction: we want an *intermediate representation*

# Single Static Assignment (SSA)

Each variable has **exactly one definition** in the syntax<sup>1</sup>

Use-def chains are explicit in the syntax of the program -> Many optimizations are simplified

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Introduced in 1988:

“Global value numbers and redundant computations” by Rosen, Wegman and Zadeck

Used in most modern compilers: GCC, llvm, HotSpot...

We will consider here more specifically Control Flow Graphs in SSA form

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# Converting to SSA form: informally

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# Converting straight code

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a <- x + 1
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a <- a + b
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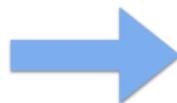
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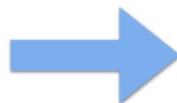
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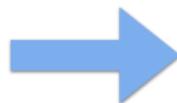
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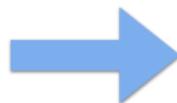
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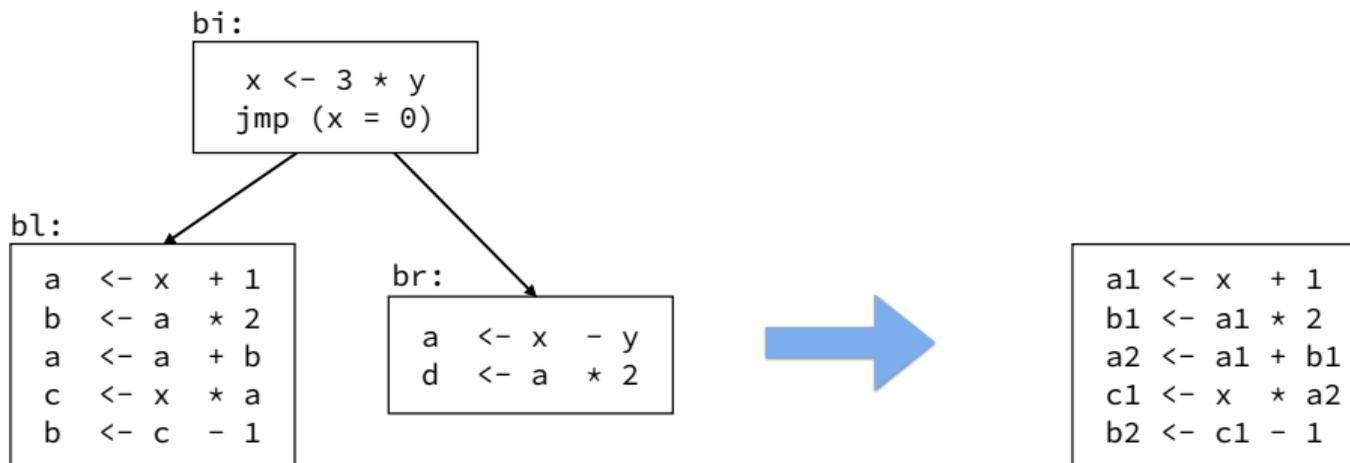
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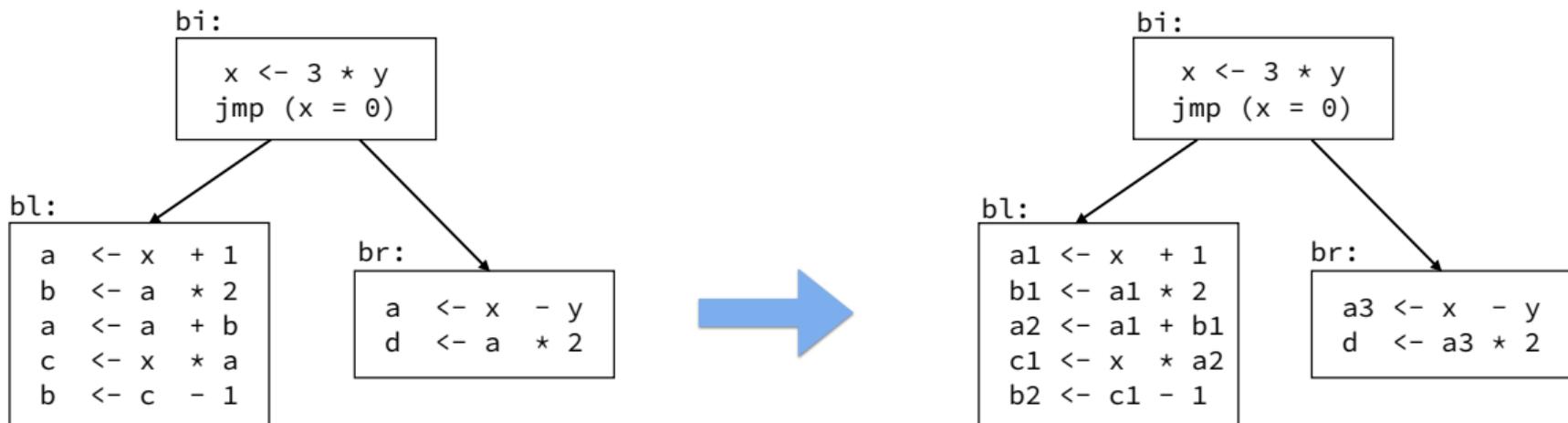
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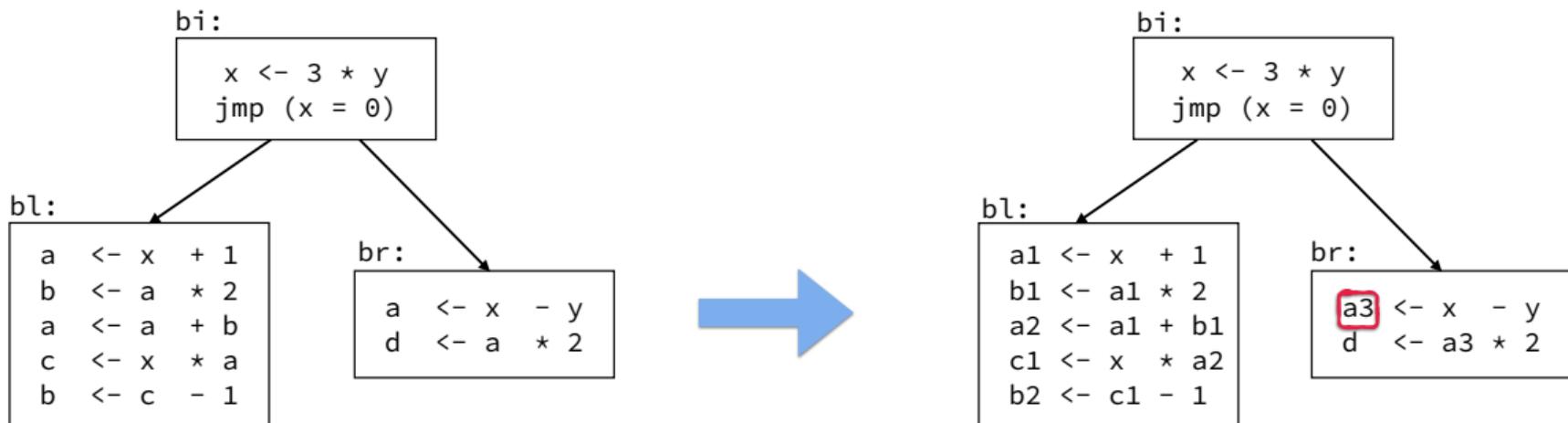
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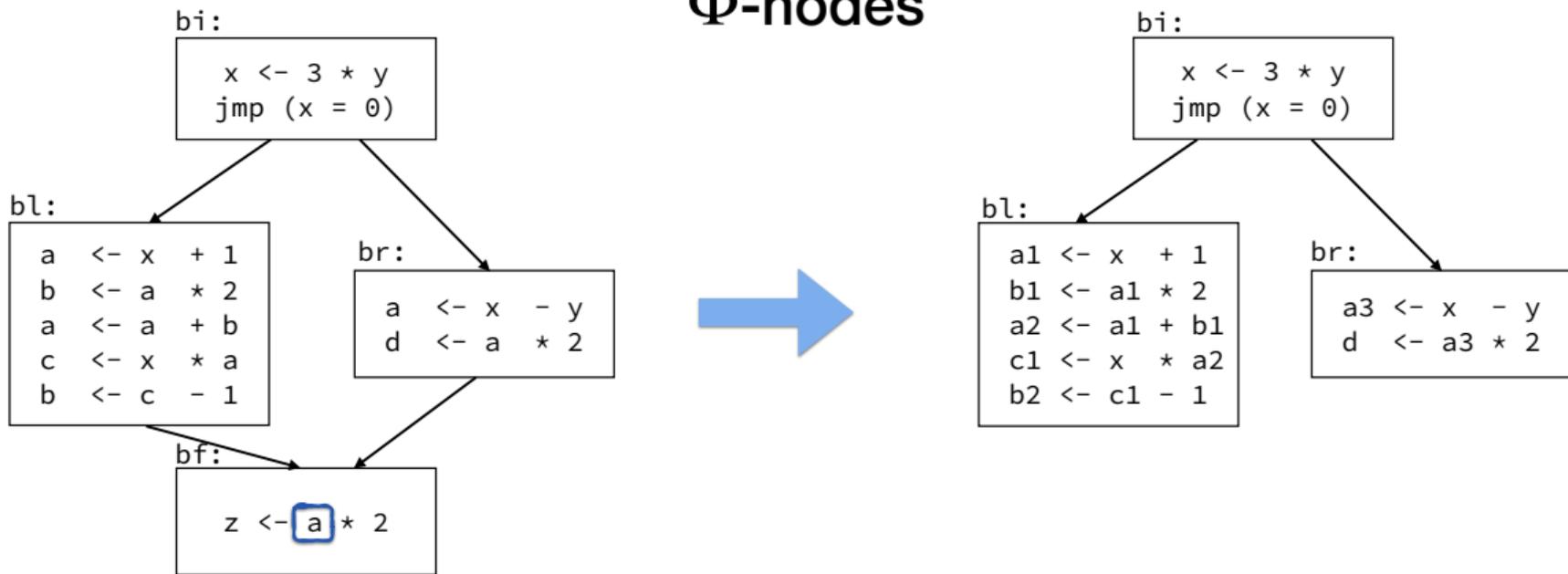


Freshness is global

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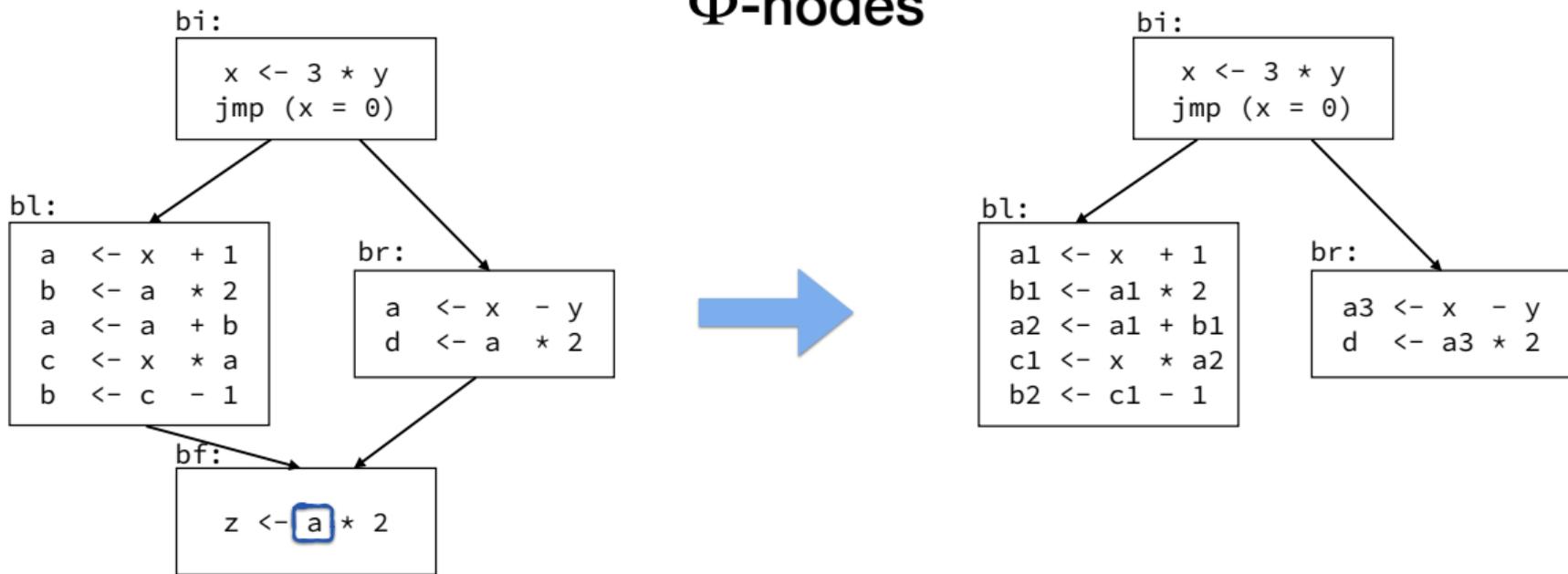
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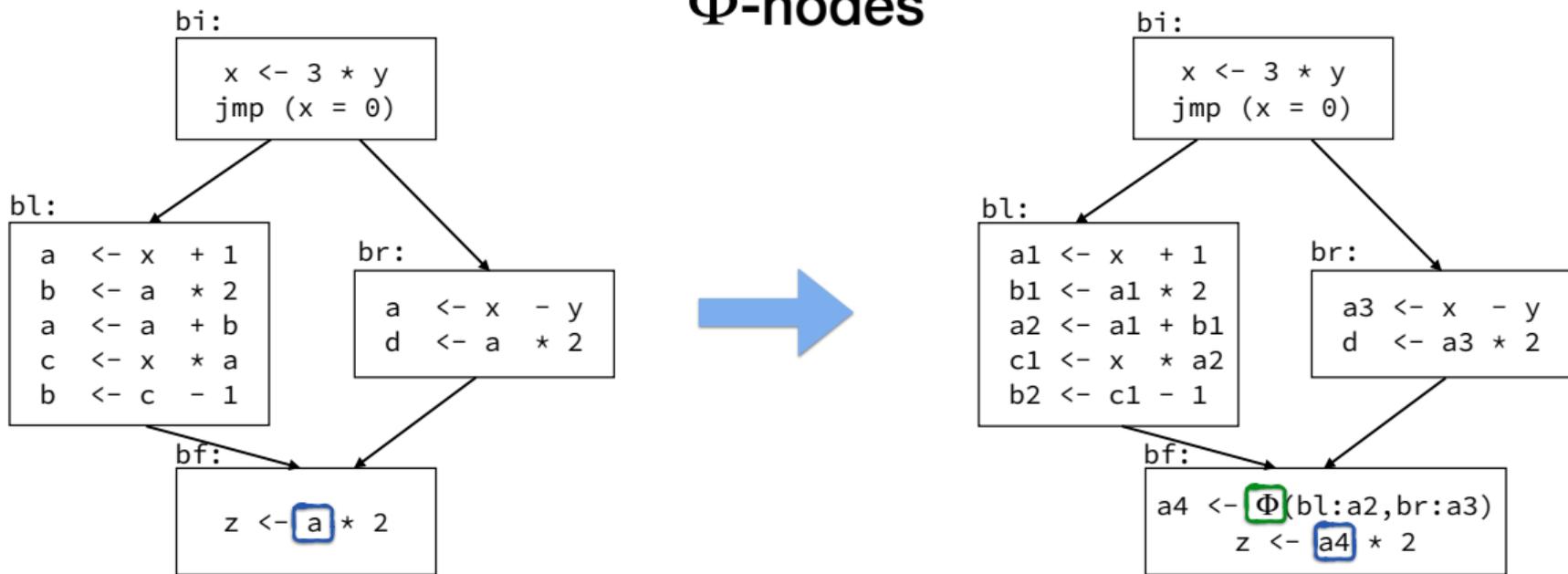


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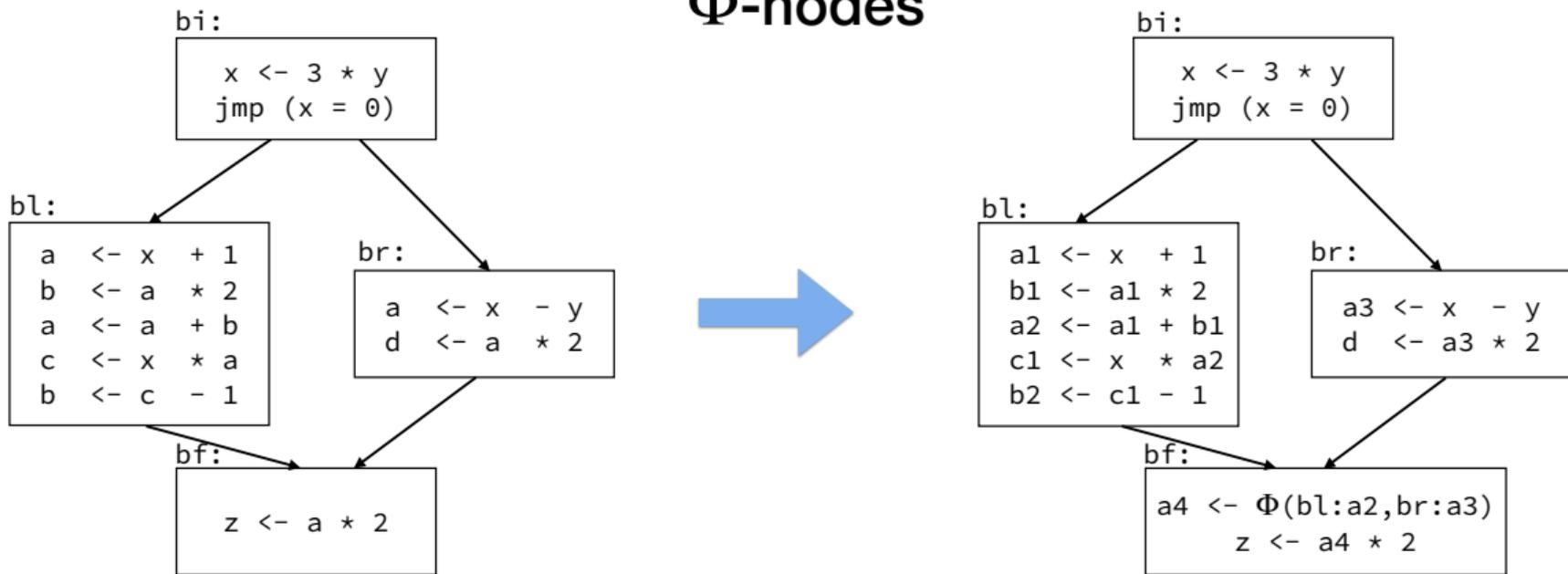


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**Rule 3:** at merge points, introduce  $\Phi$ -nodes

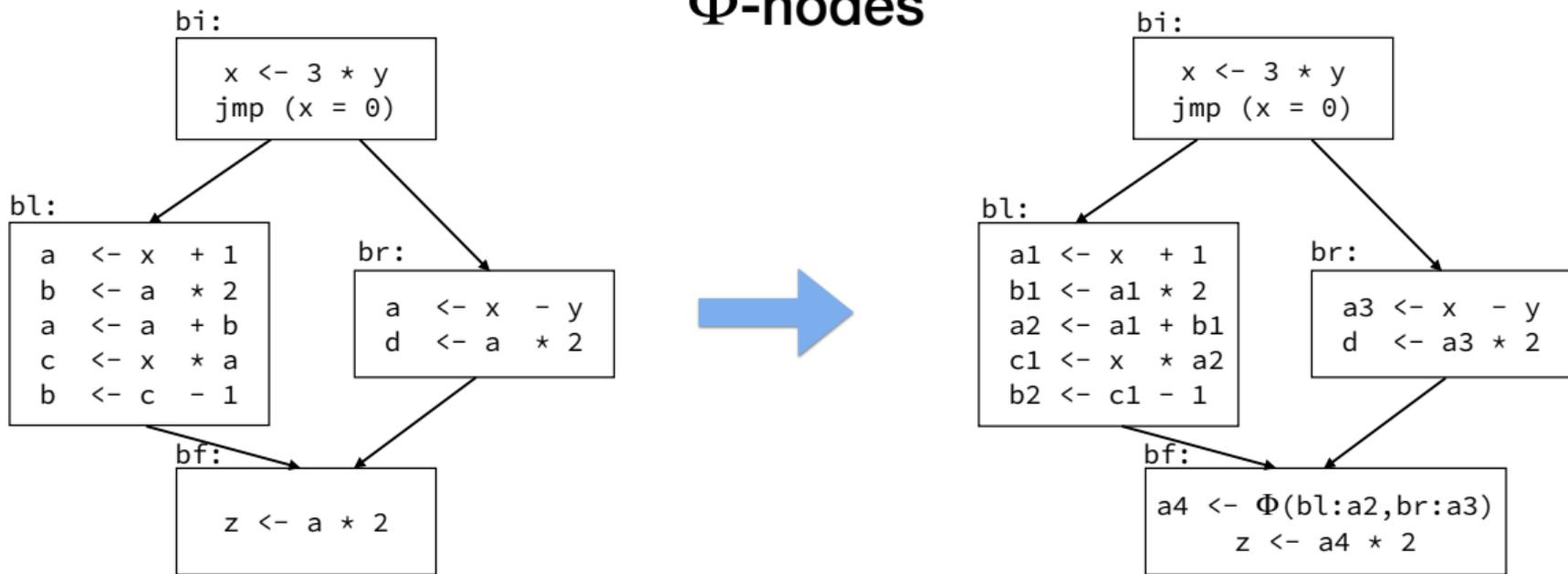
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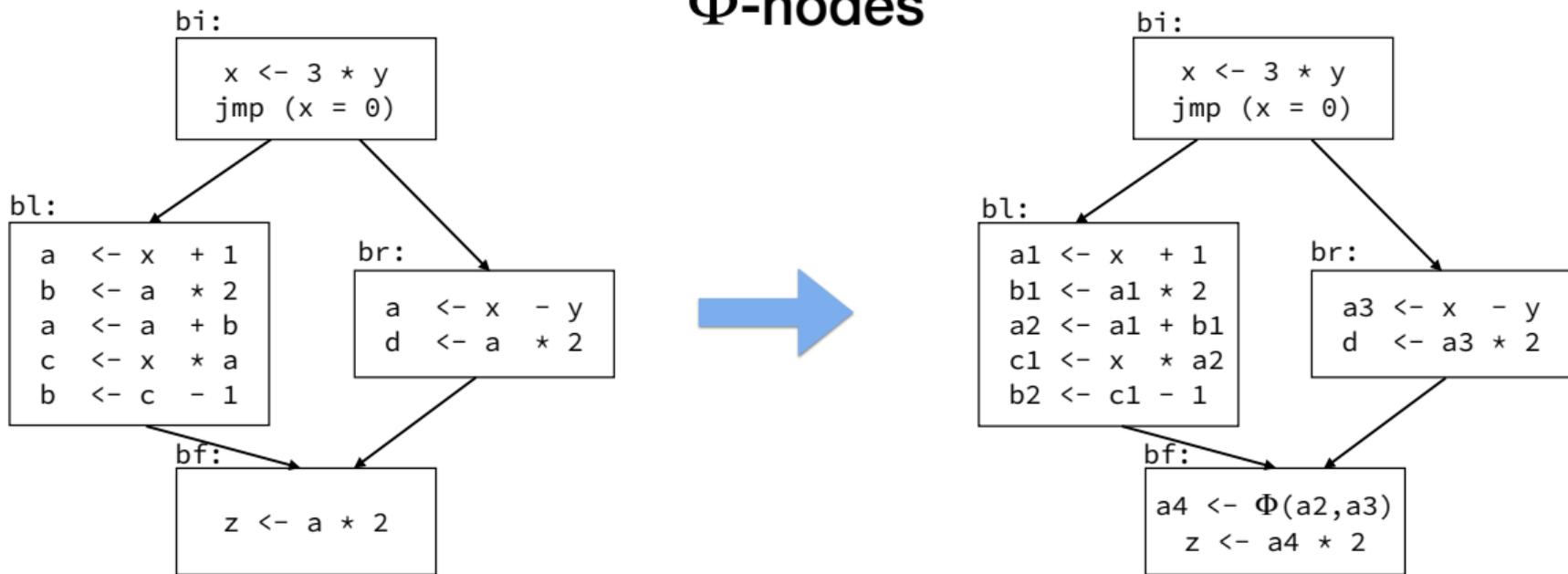
## $\Phi$ -nodes



Goal: to decide when to introduce  $\Phi$ -nodes

One per variable at every join point?

# Converting merging points: $\Phi$ -nodes



Goal: to decide when to introduce *few*  $\Phi$ -nodes

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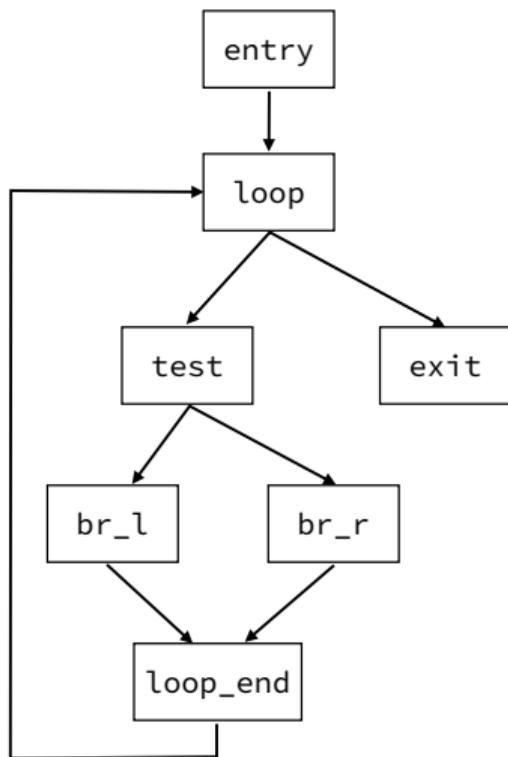
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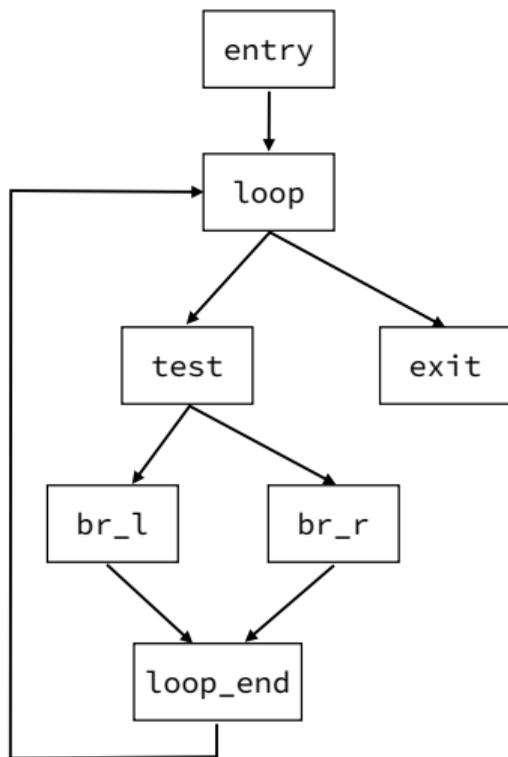
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A dominates B if any path from entry to B contains A



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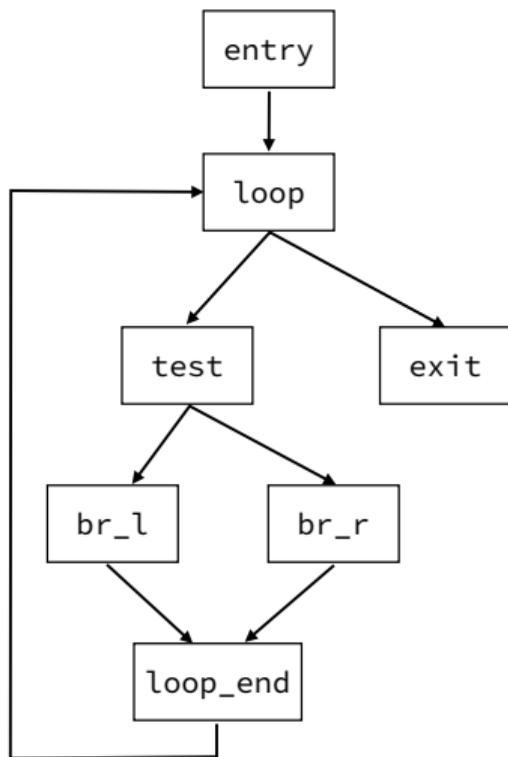
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Can you annotate the nodes with their dominators?

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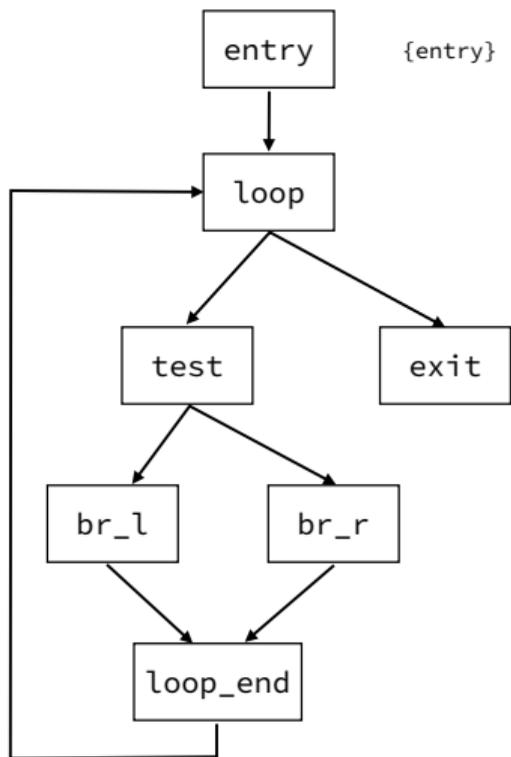
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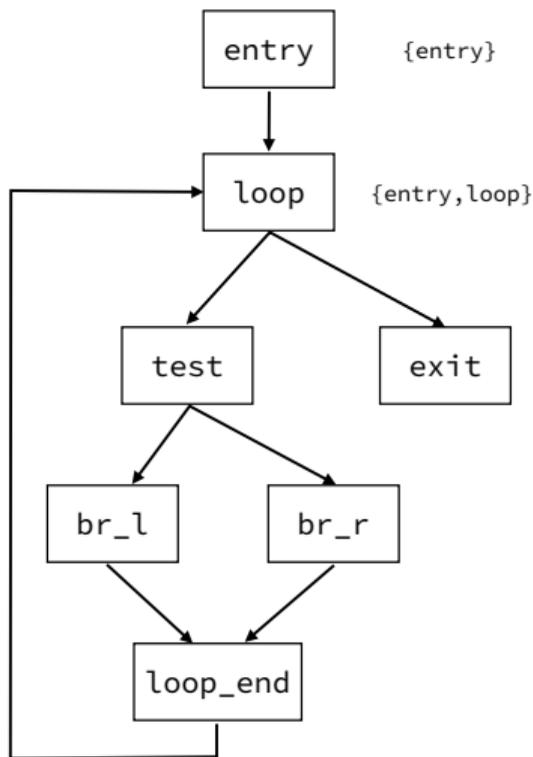
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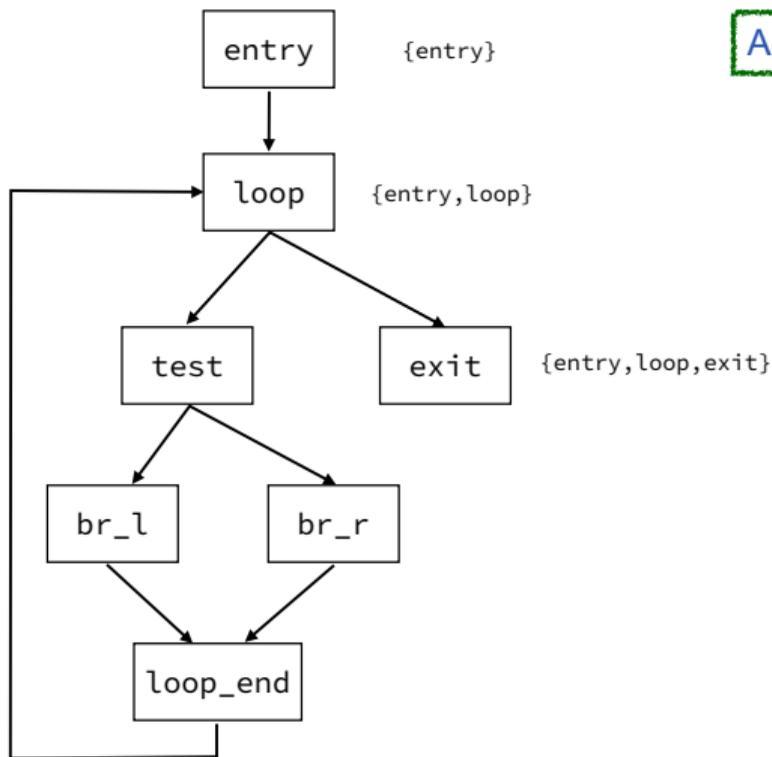
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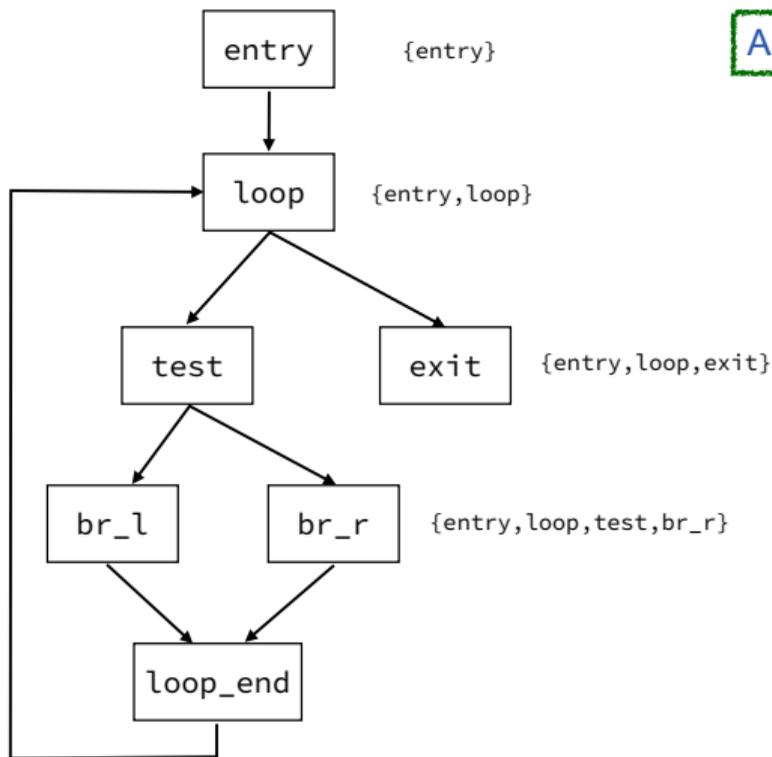
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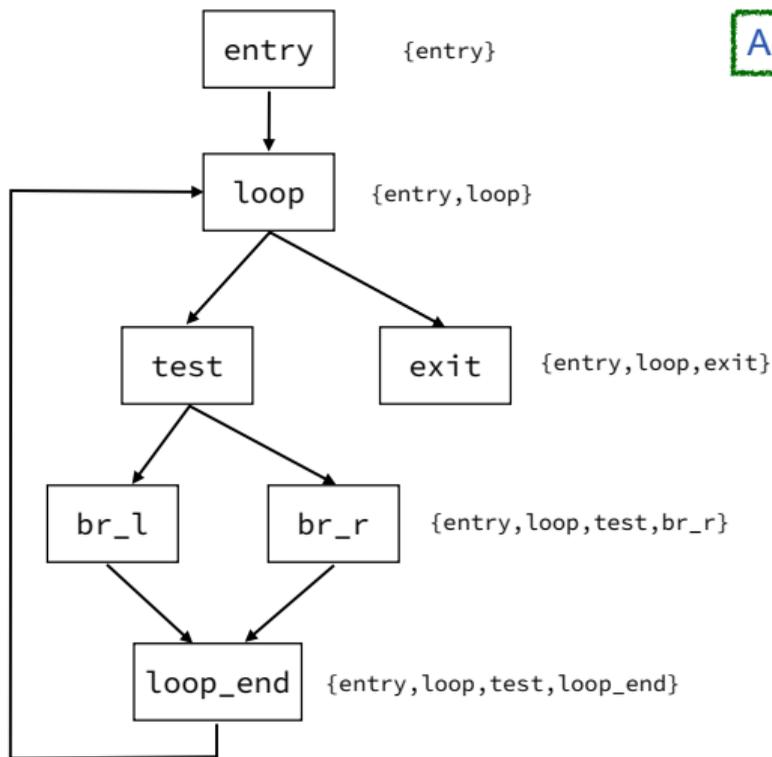
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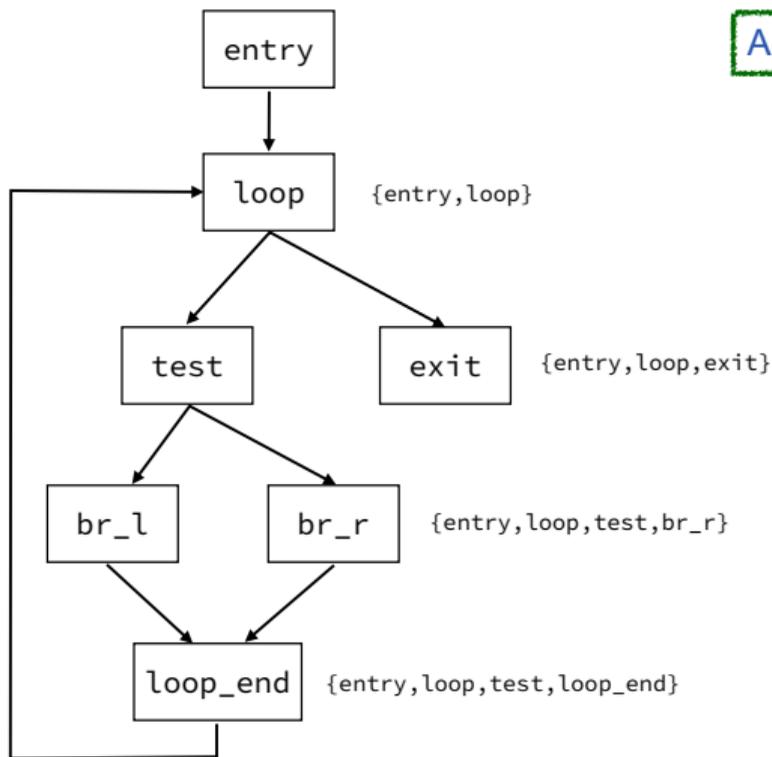
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A dominates B if any path from entry to B goes through A

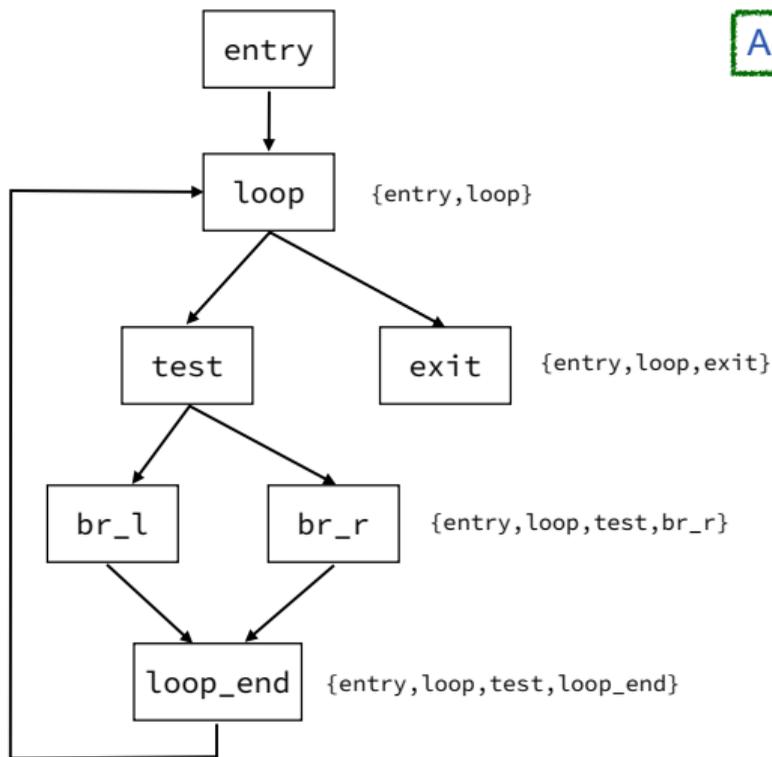
A *strictly dominates* B if A dominates B and A is not B

The *domination tree* stores the domination relation

A is parent to B if:

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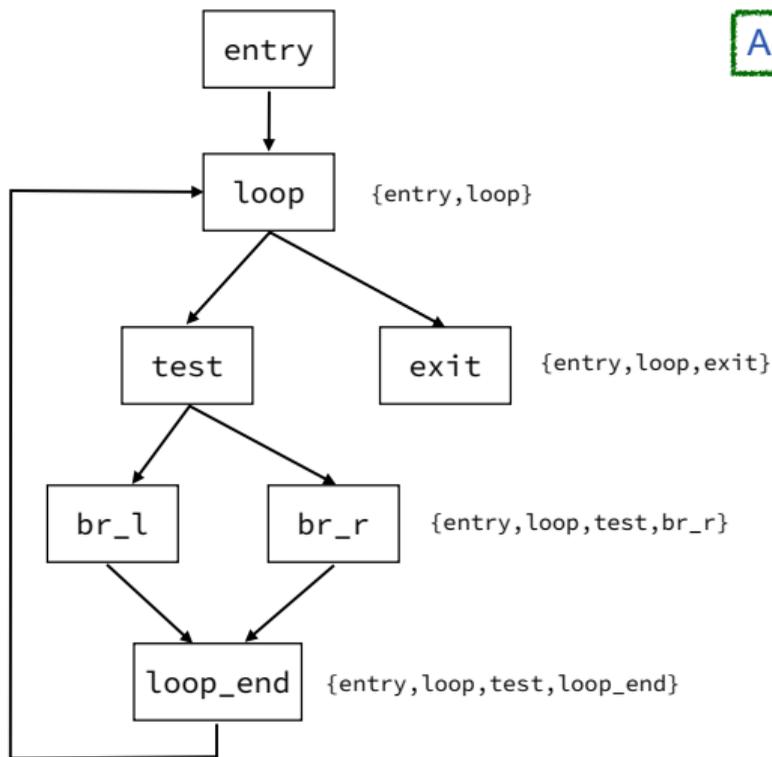
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Can you build the domination tree of the CFG to the left?

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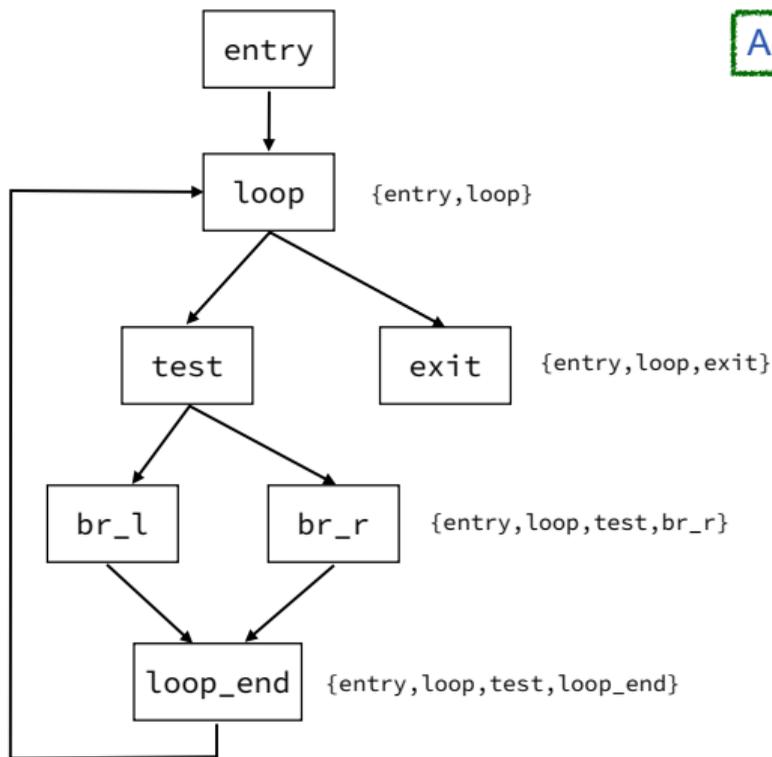
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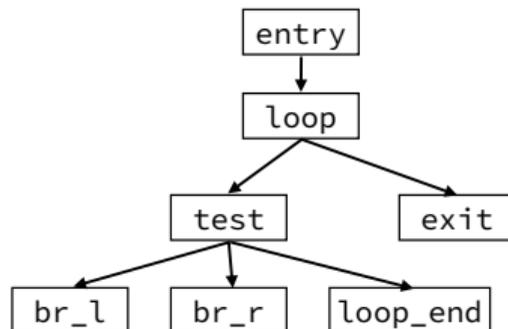
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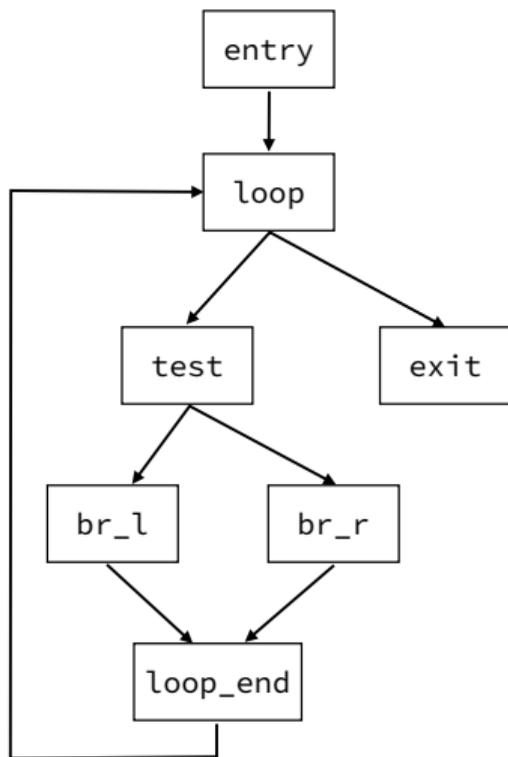
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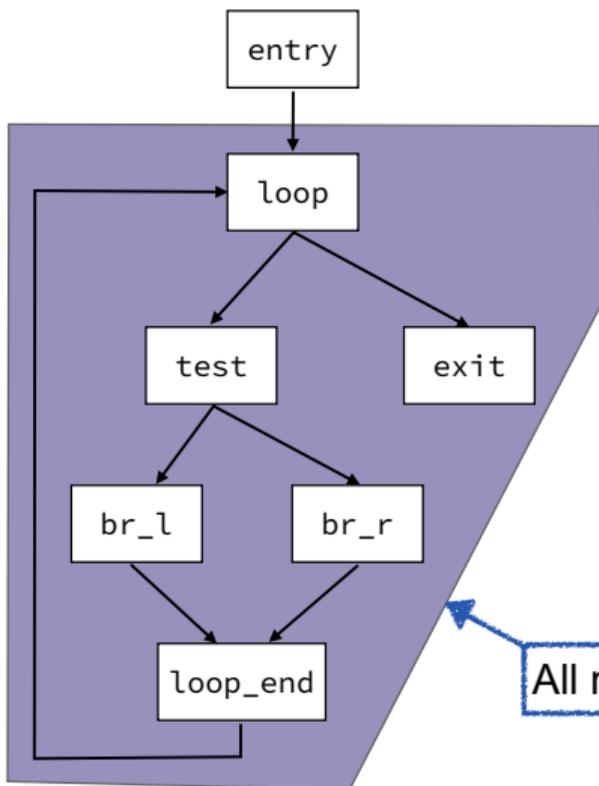


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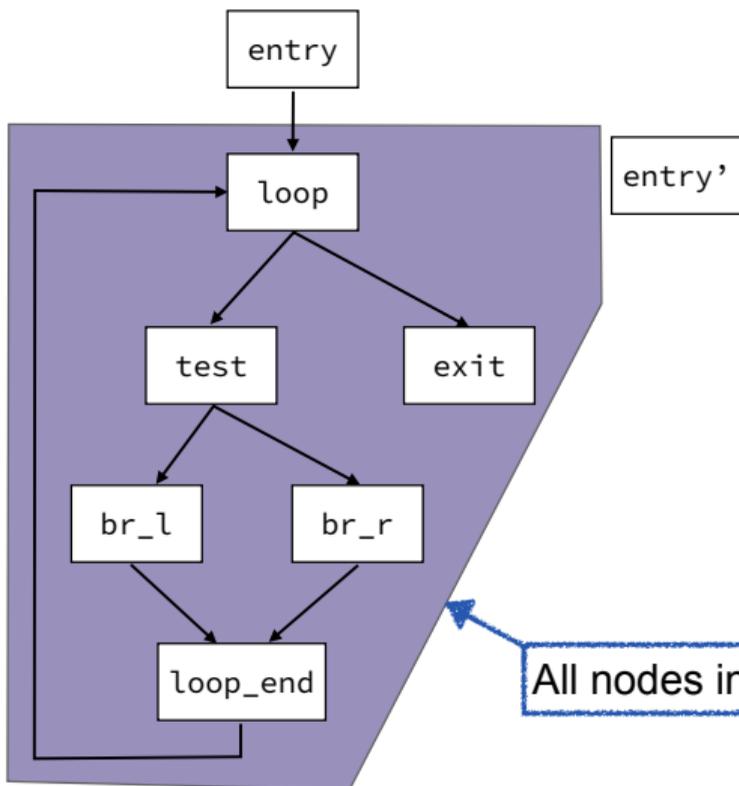
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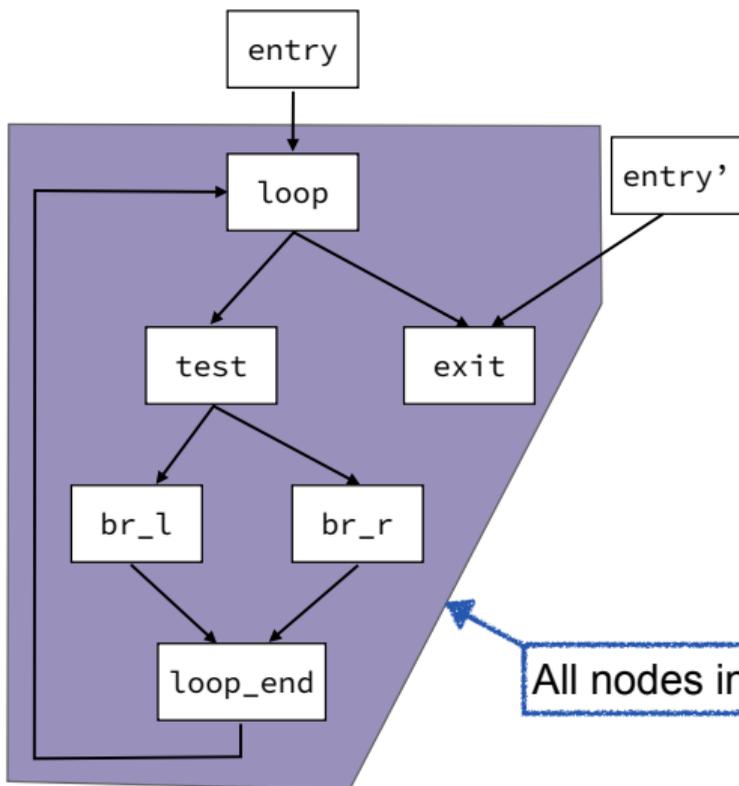
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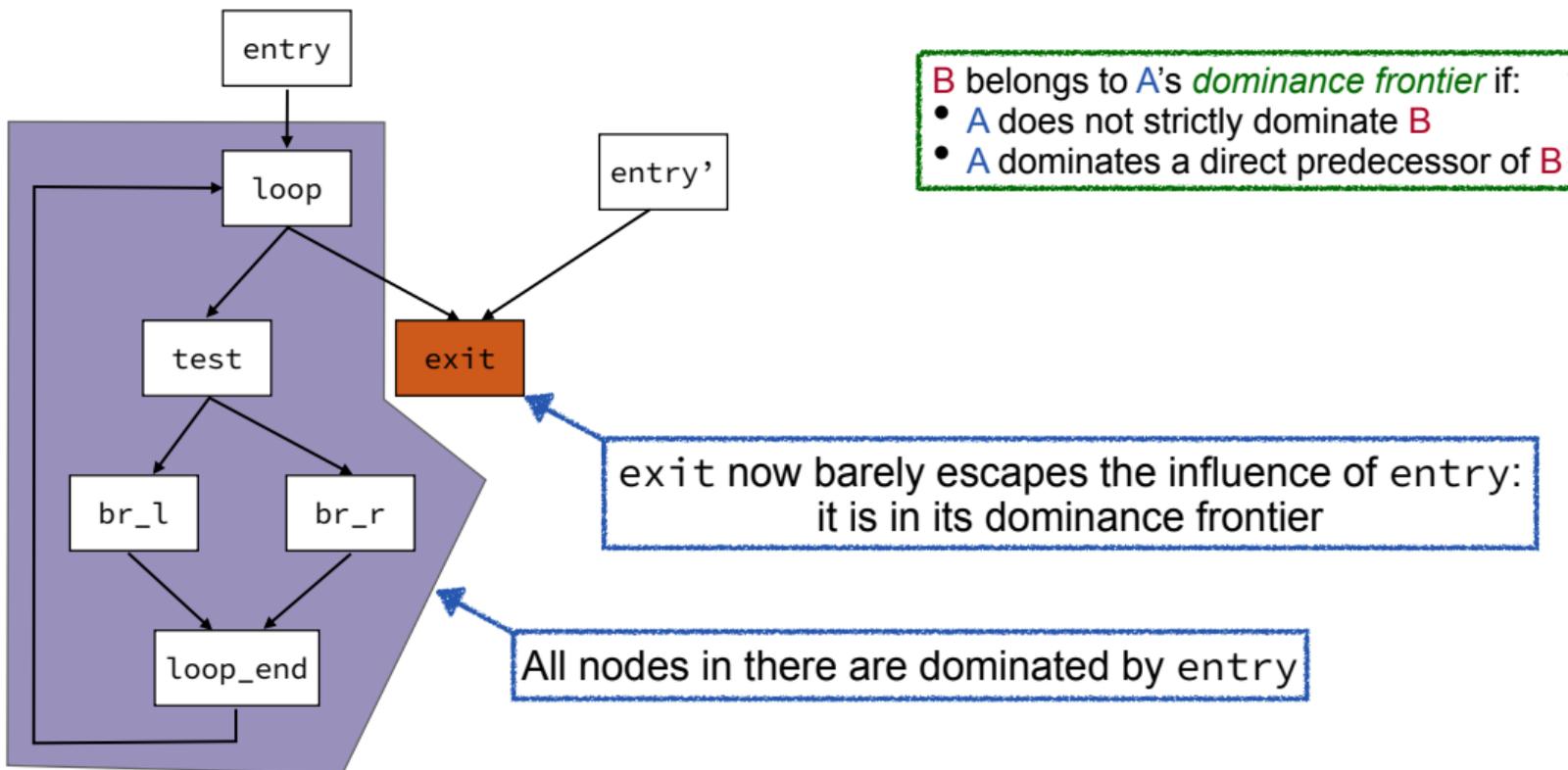
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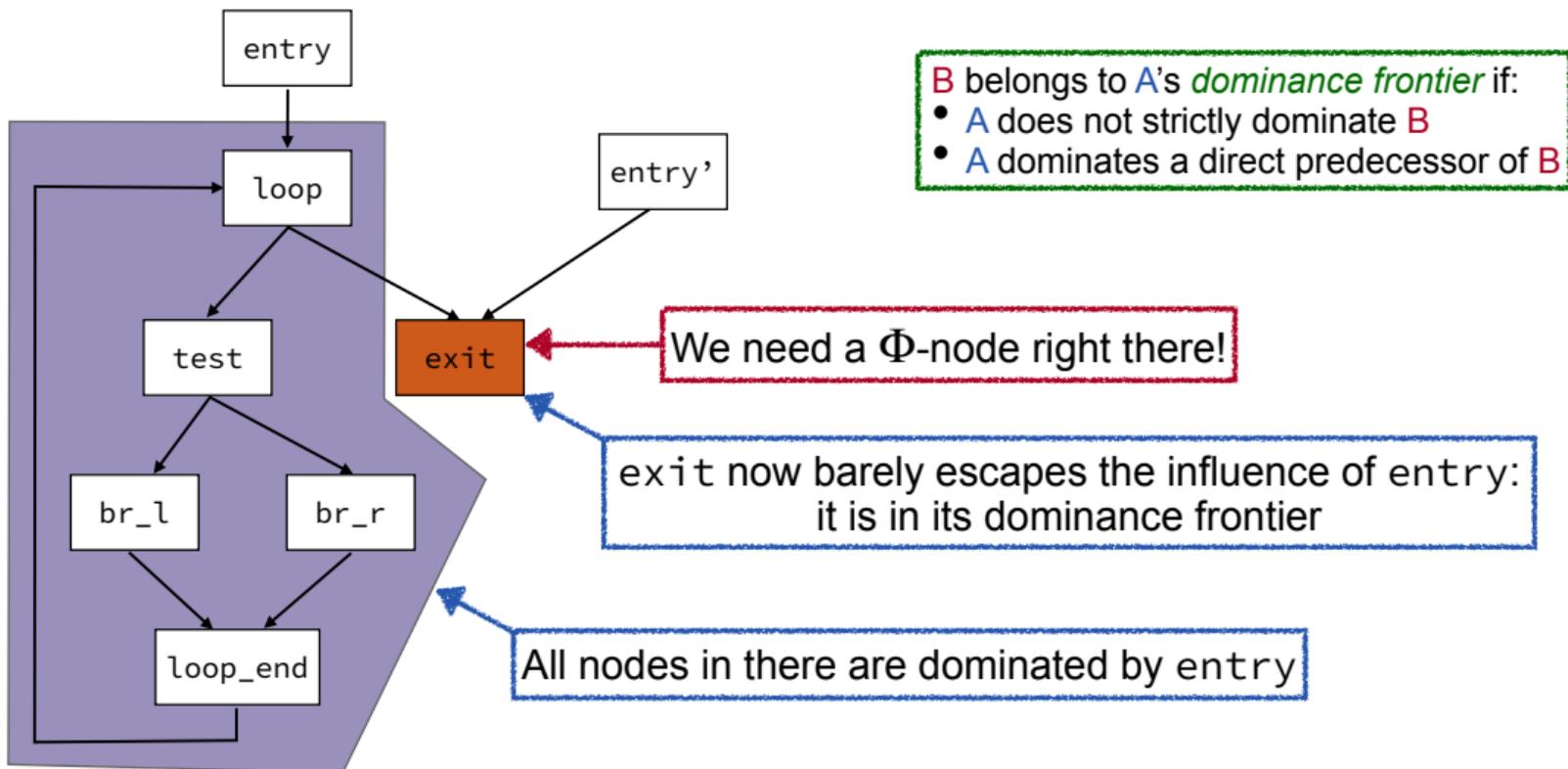
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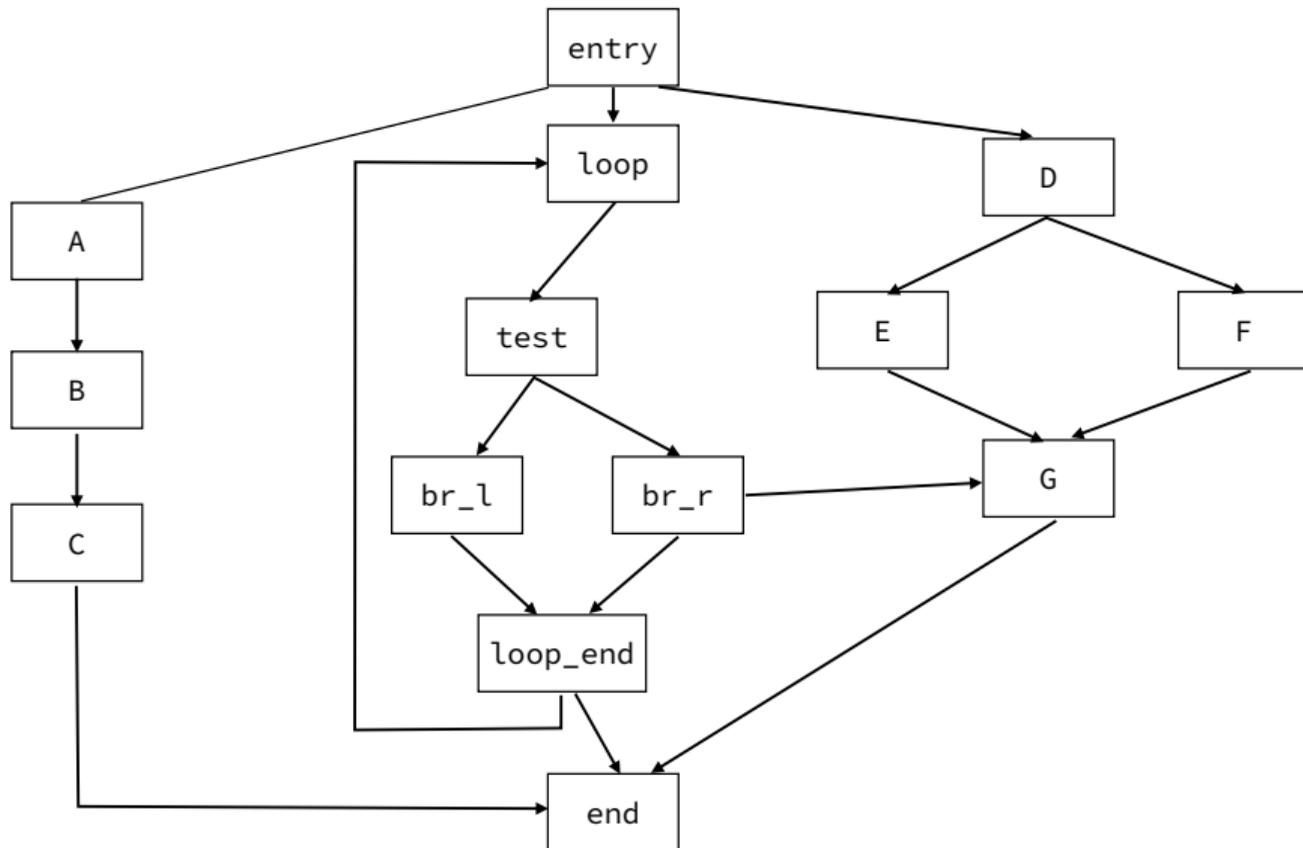
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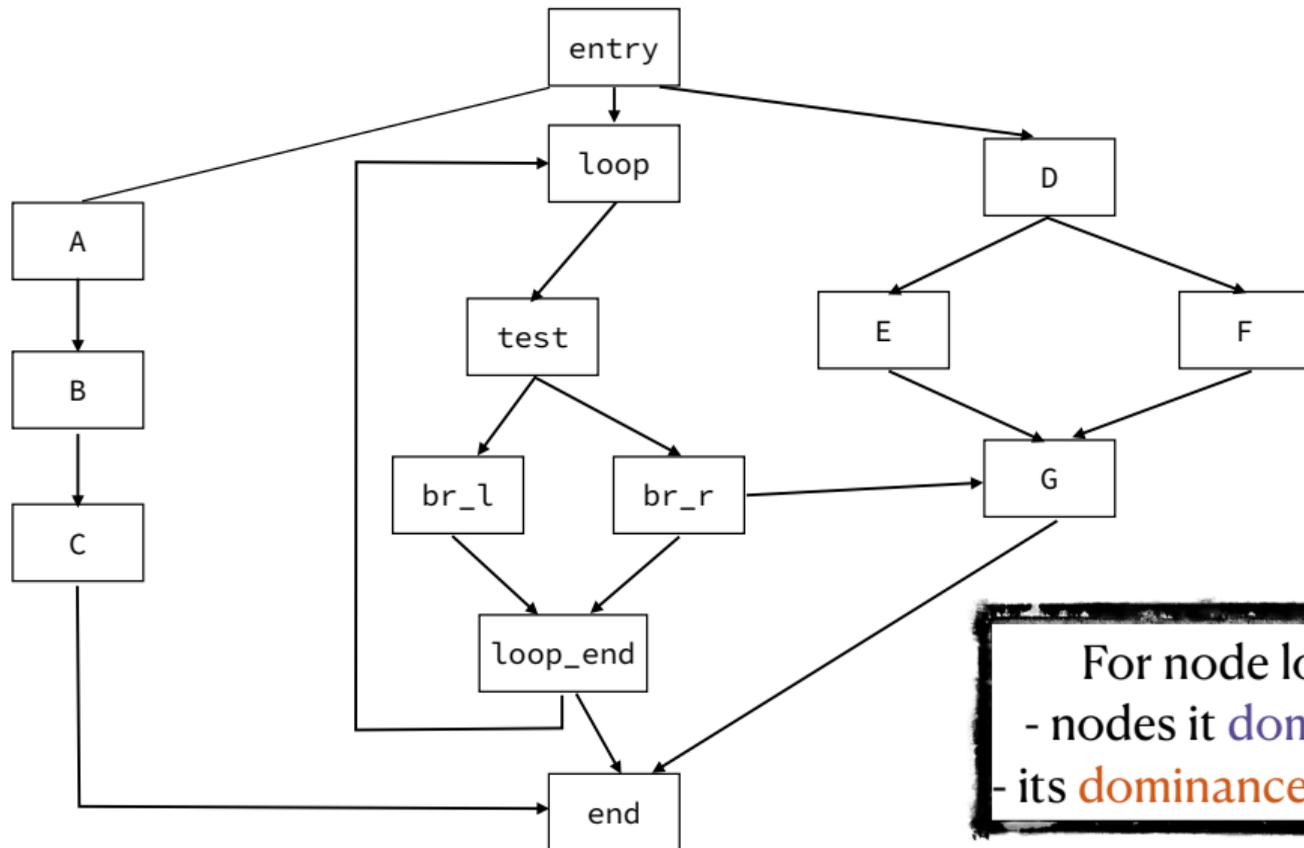
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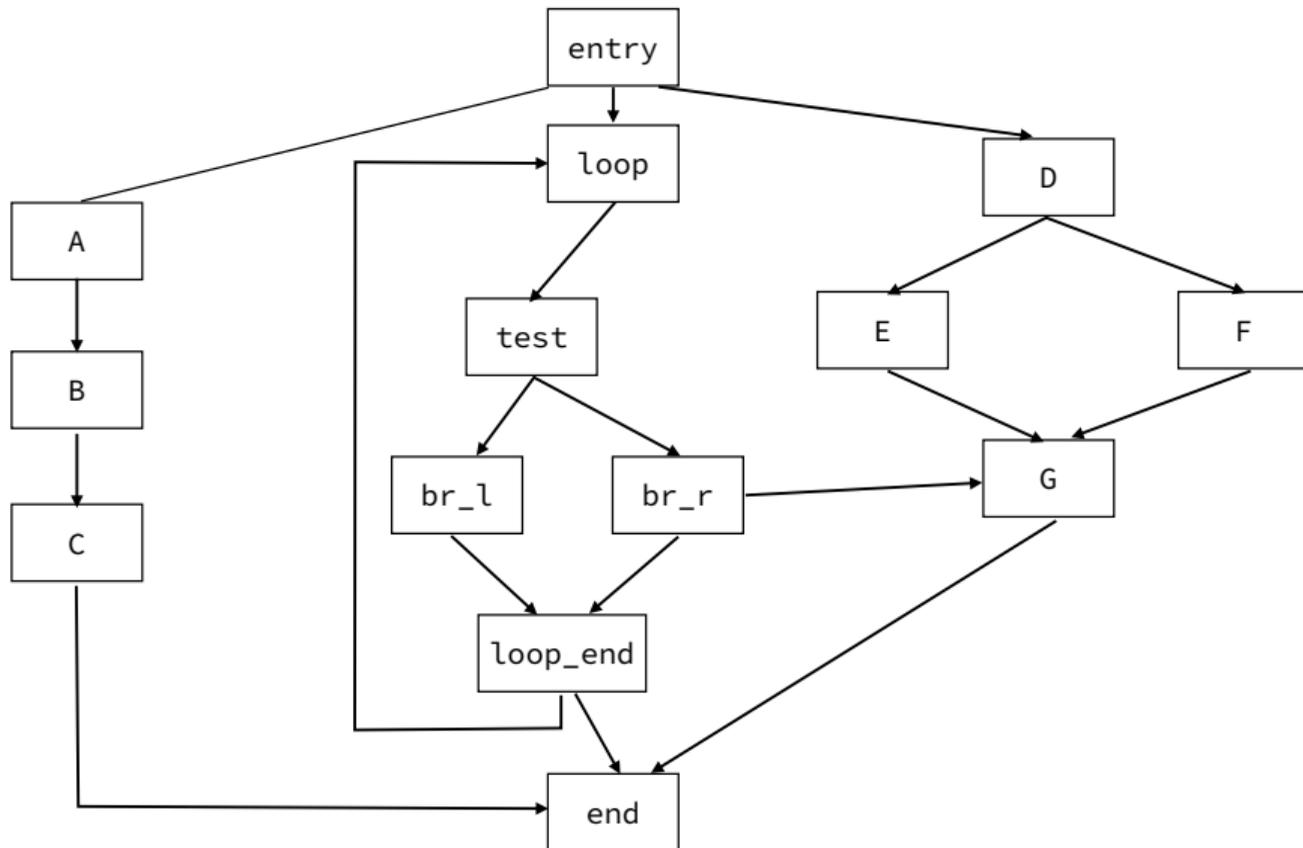
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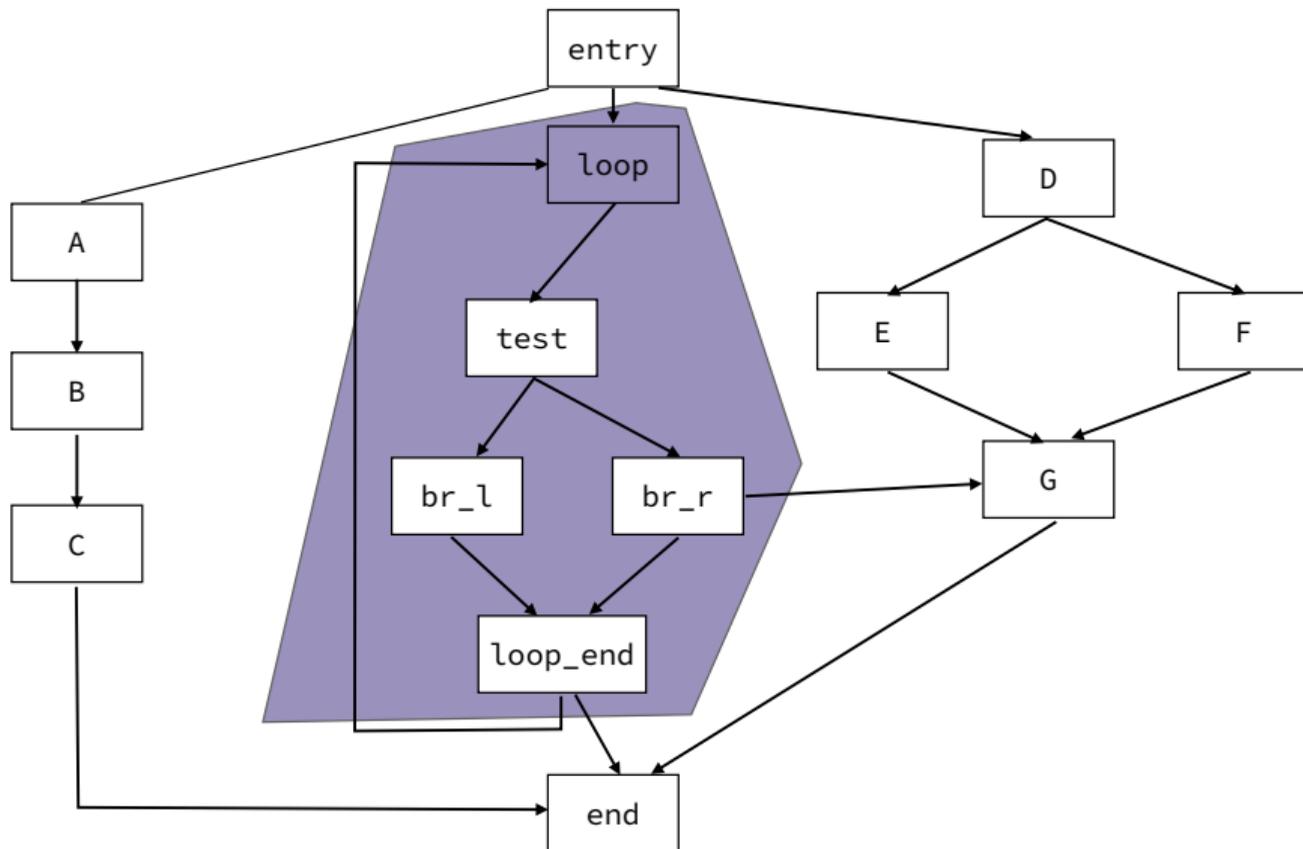
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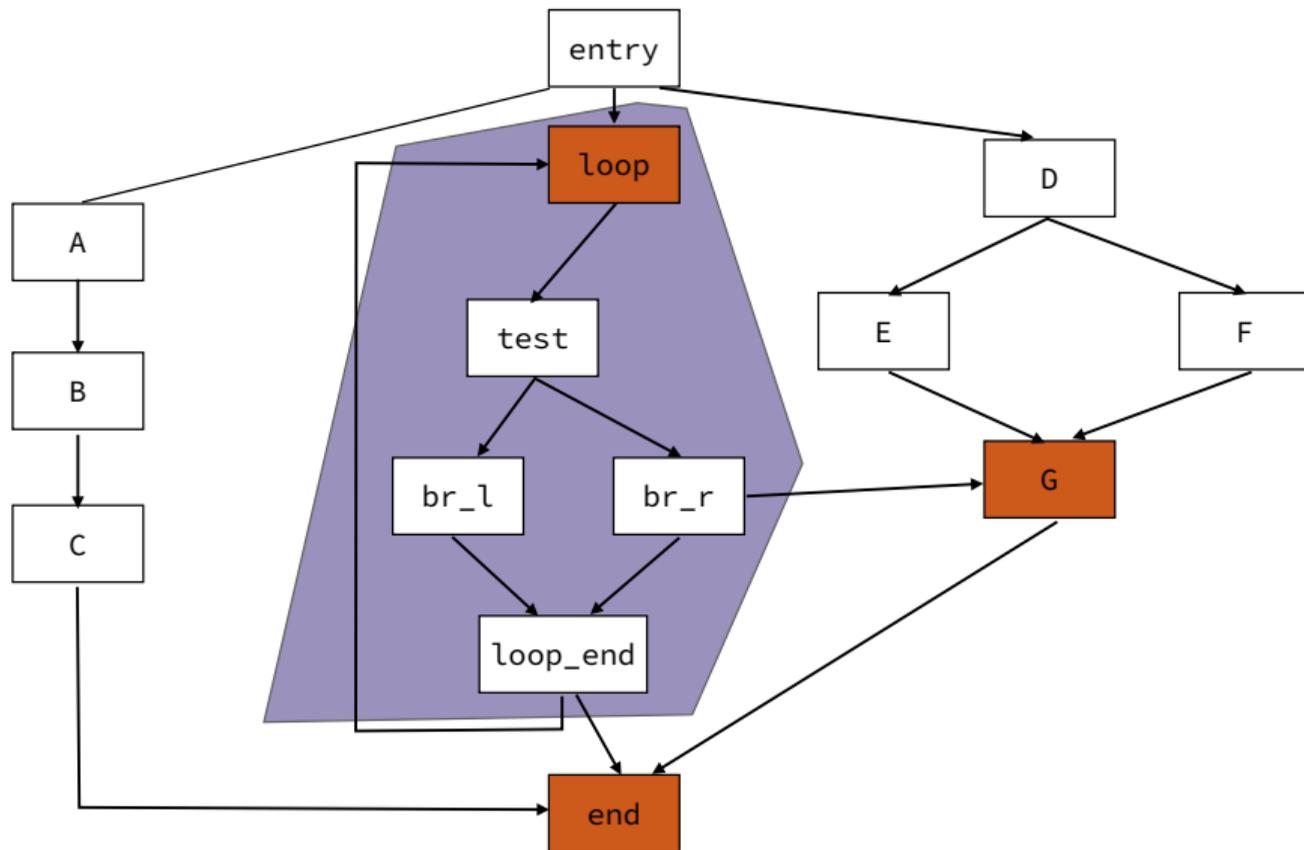
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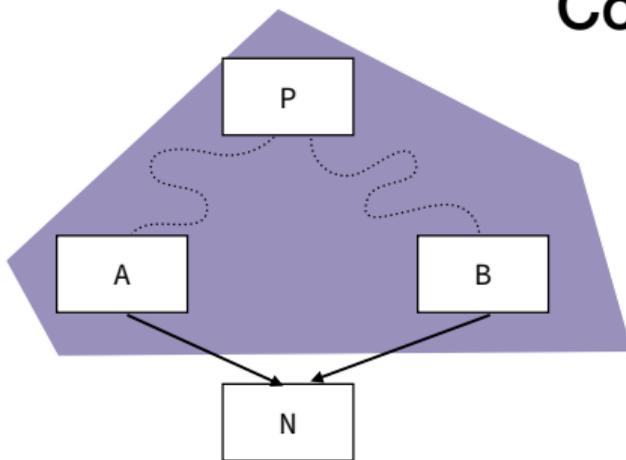
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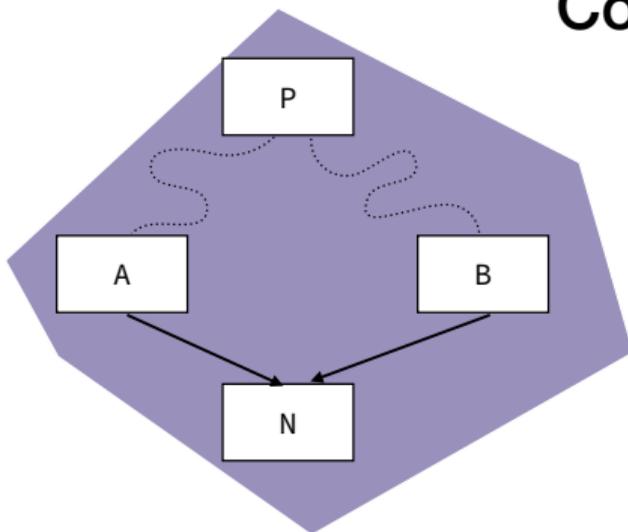


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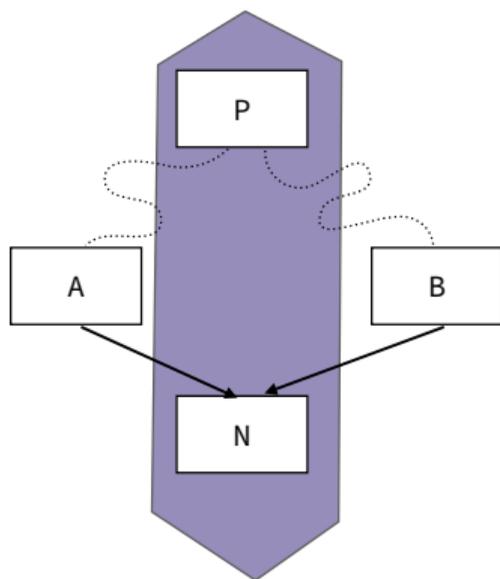
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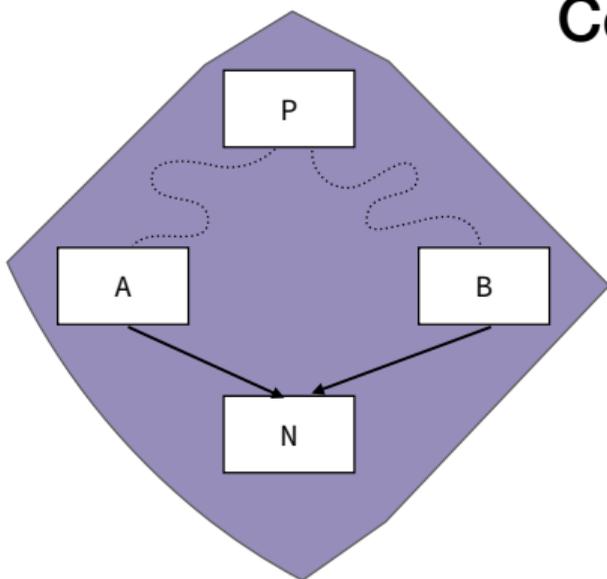
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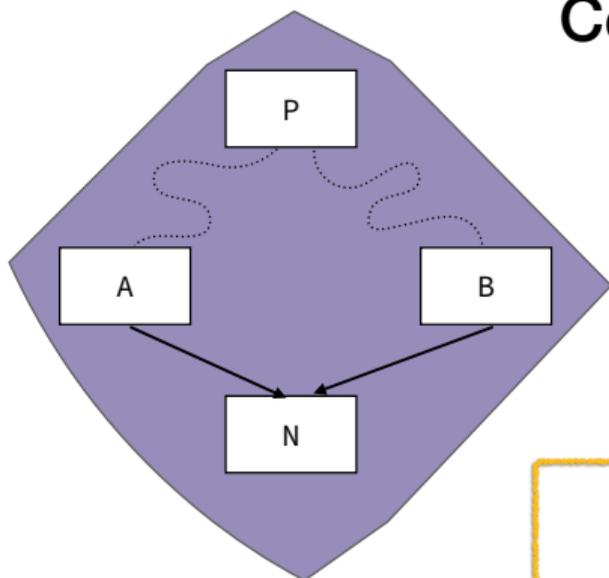
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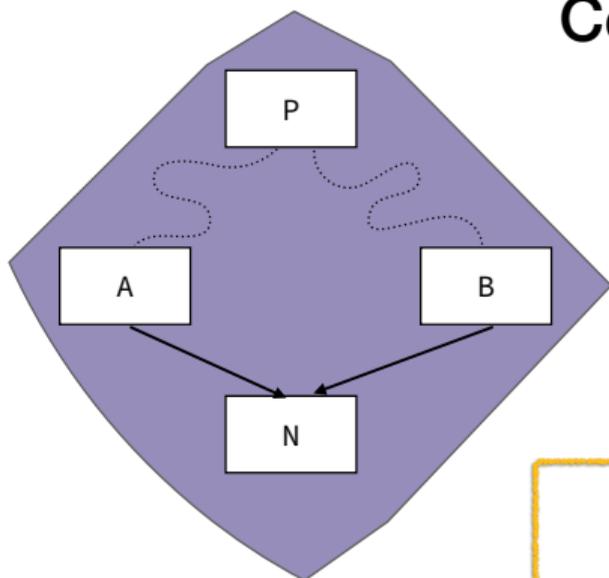
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**Complexity?**

<sup>1</sup>: For a more efficient algorithm, see Lengauer and Tarjan's 1979

"A fast algorithm for finding dominators in a flowgraph"

# Computing the dominance frontier

$G$  : ambient cfg

DT: Dominance Tree of  $G$

DF: map from nodes to sets of nodes

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`S ← {y | y successor of n in G but not in DT}`

“Obvious”, immediate frontier



## Computing the dominance frontier

$G$  : ambient cfg

$DT$ : Dominance Tree of  $G$

$DF$ : map from nodes to sets of nodes

`computeDF(n) ::=`

$S \leftarrow \{y \mid y \text{ successor of } n \text{ in } G \text{ but not in } DT\}$

for  $c$  in  $\text{children}(n)$  in  $DT$ :

`computeDF(c)`

  for each  $w$  in  $DF[c]$ :

    if  $n$  does not dominate  $w$ :

$S \leftarrow S \cup \{w\}$

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The rest of the frontier is inherited from the other children



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`for each w in DF[c]:`

`if n does not dominate w:`

`S ← S ∪ {w}`

`DF[n] ← S`

“Obvious”, immediate frontier



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# Computing the dominance frontier

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$DT$ : Dominance Tree of  $G$

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$S \leftarrow S \cup \{w\}$

$DF[n] \leftarrow S$

$DF ::= computeDF(entry)$

“Obvious”, immediate frontier



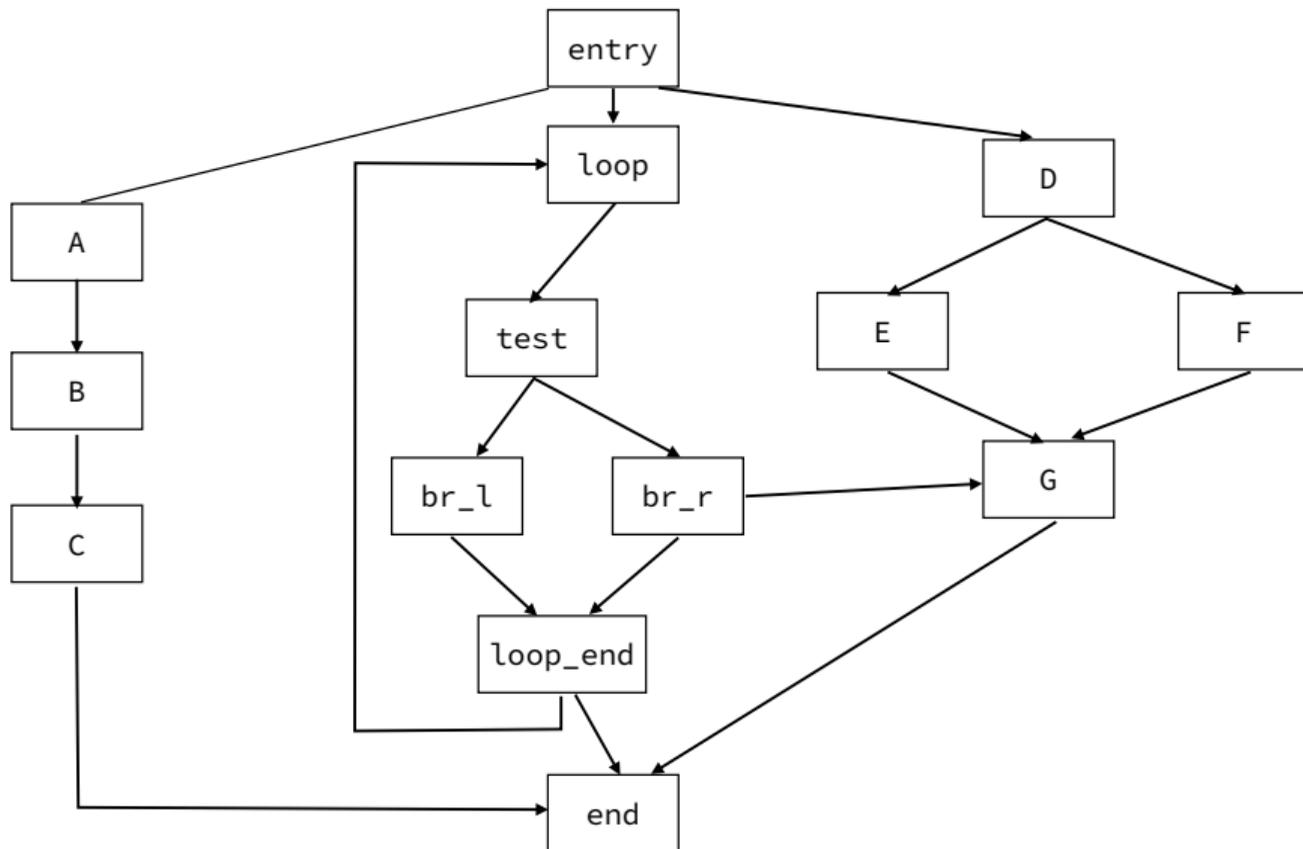
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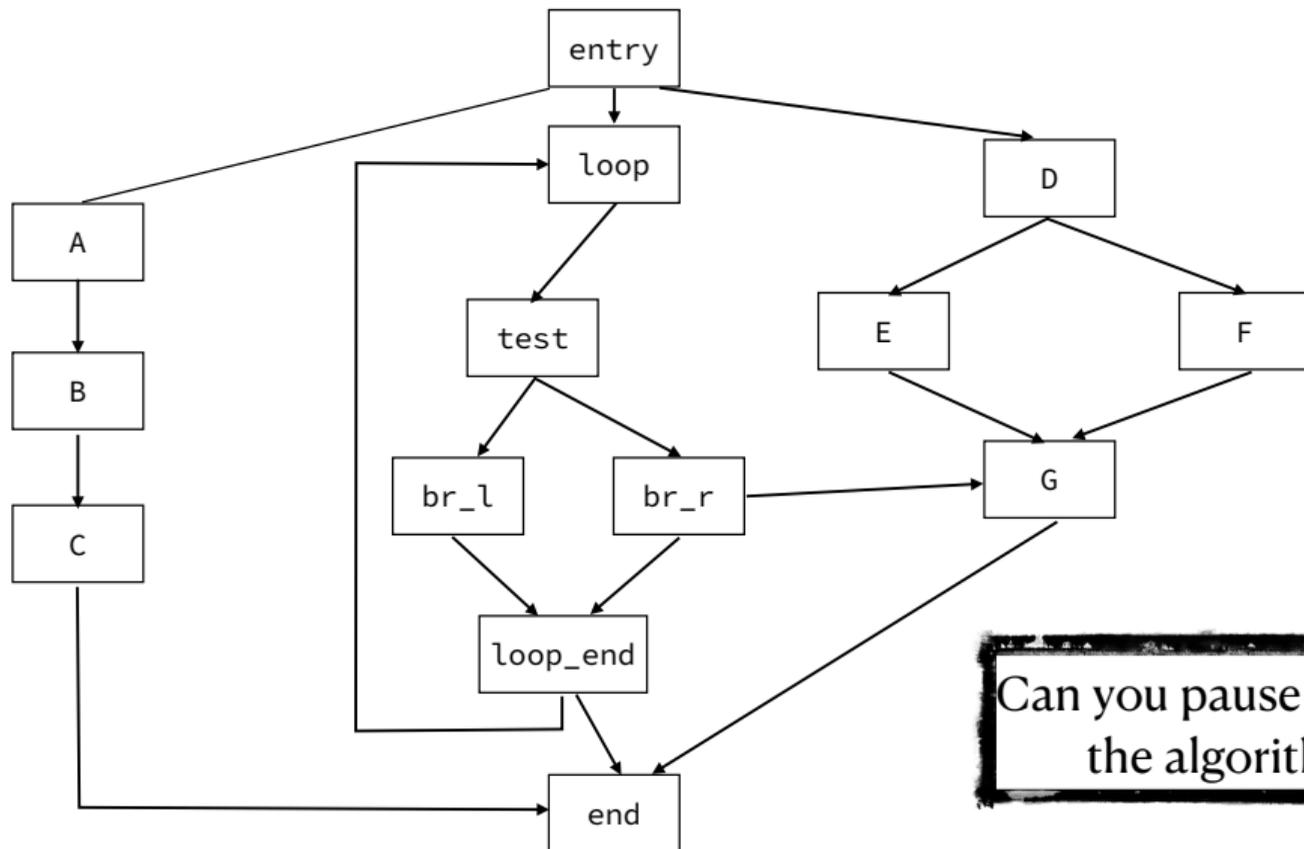
We kickstart the pass from the entry



# Computing the dominance frontier

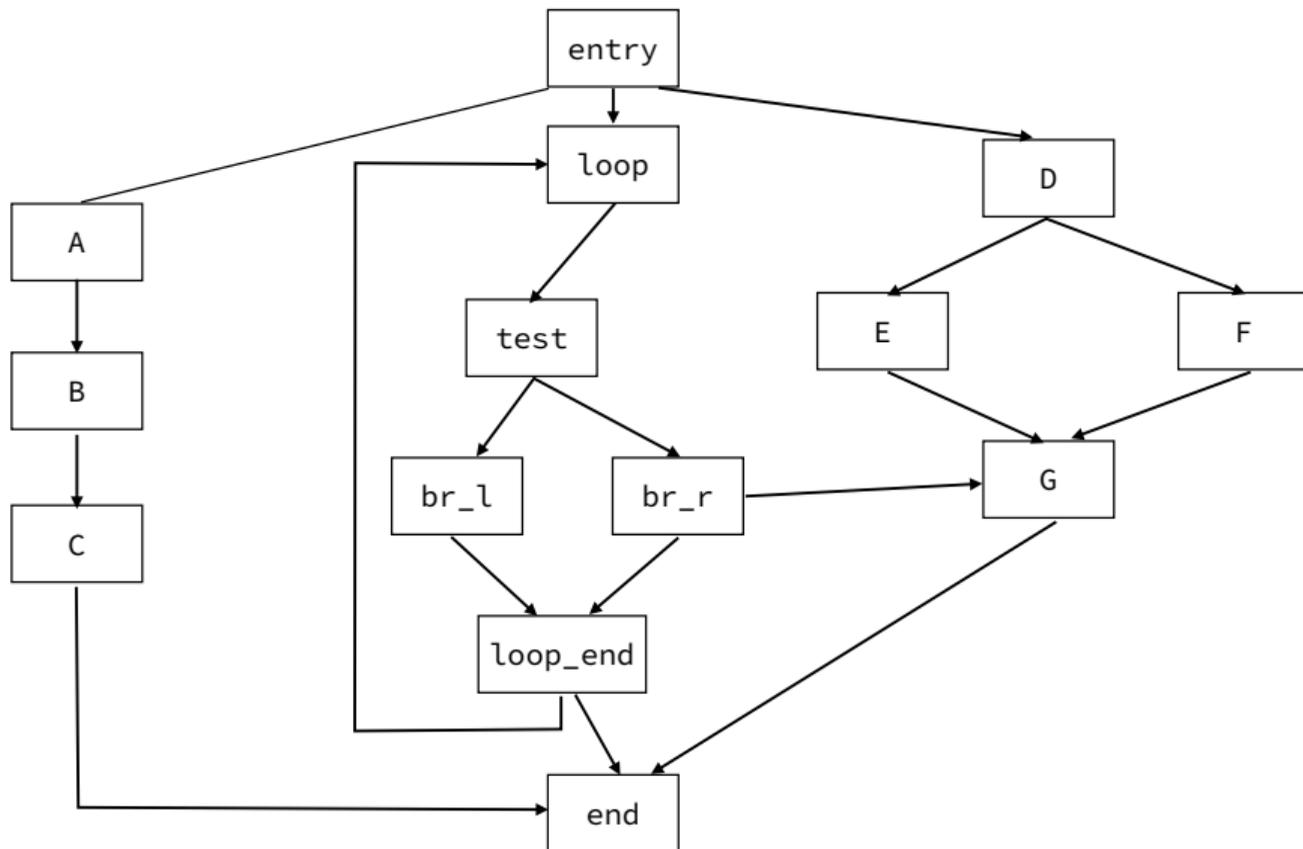


# Computing the dominance frontier



Can you pause and run the algorithm?

# Computing the dominance frontier

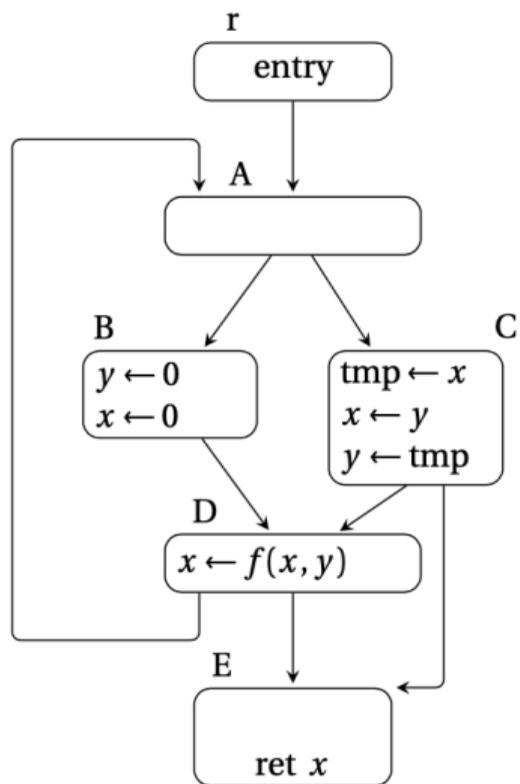


# Taking stock

We want to convert a cfg to SSA-form

- The key difficulty is to figure out *where* exactly  $\Phi$ -nodes are needed
- We observed *the dominance frontier* of a node seems to be the right notion
- We saw how to construct the dominance frontier, based on the construction of *the dominance tree*

We can now turn to the construction!



# Inserting $\Phi$ -nodes

Insert-phi ::=

for  $x$  in Vars:

  for  $d$  in Defs( $x$ ):

    for  $b$  in DF( $d$ ):

      if there are no  $\Phi$ -node associated to  $x$  in  $b$ :

        add one such  $\Phi$ -node

        add  $b$  to Defs( $x$ )

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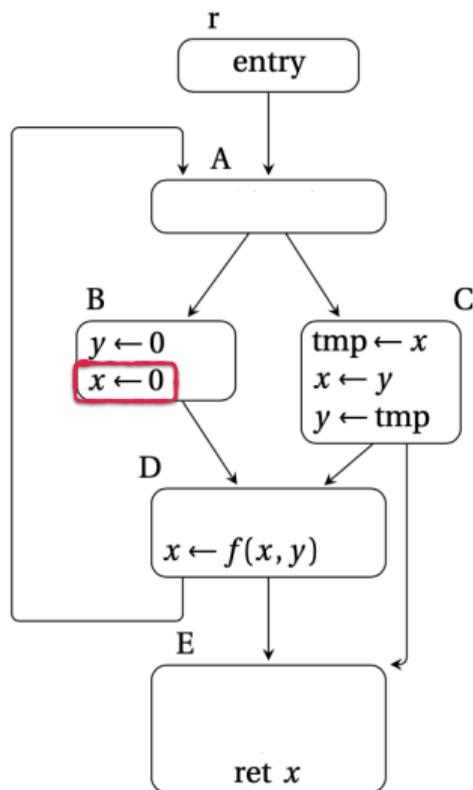
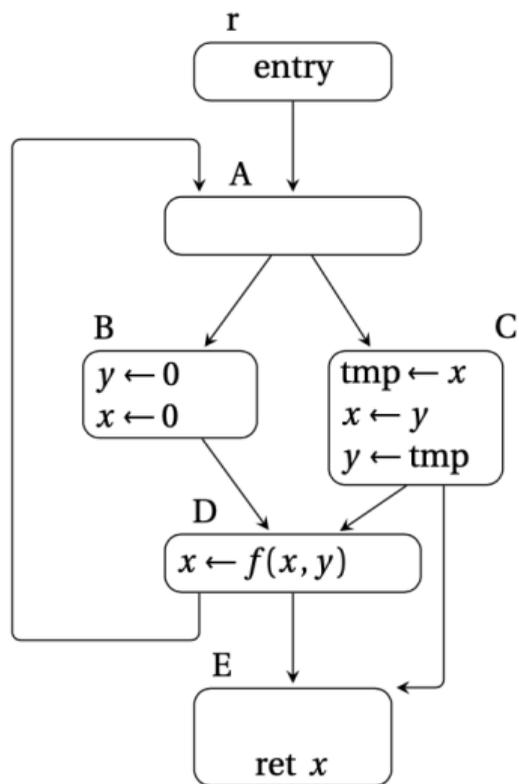
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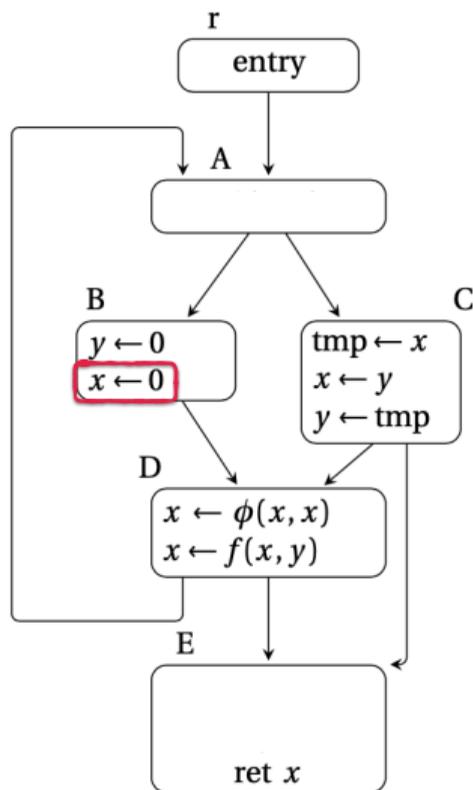
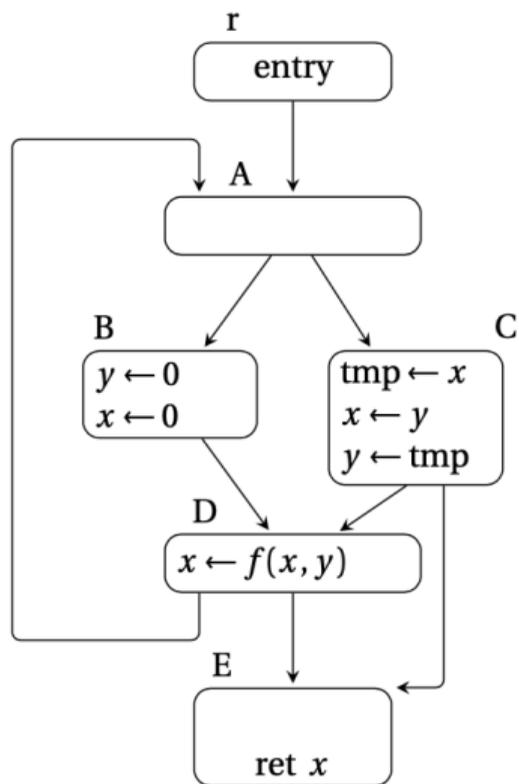
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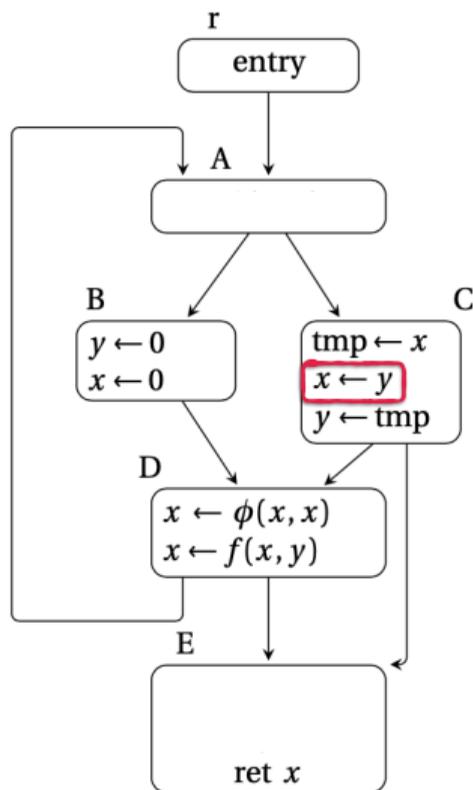
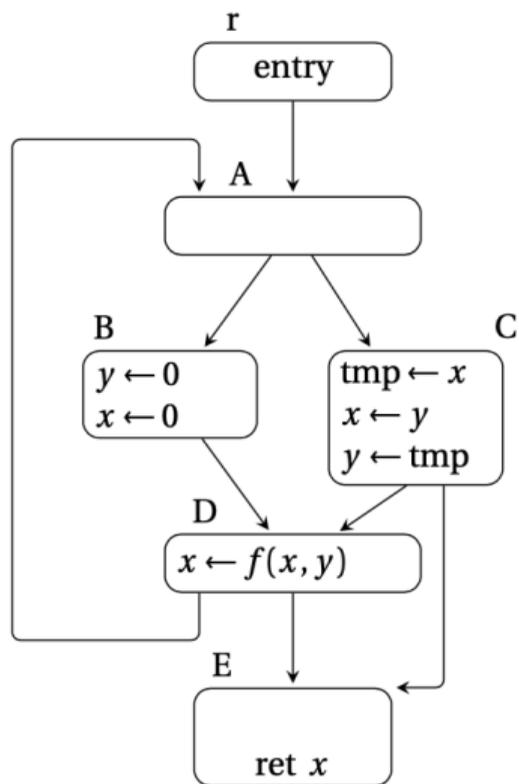
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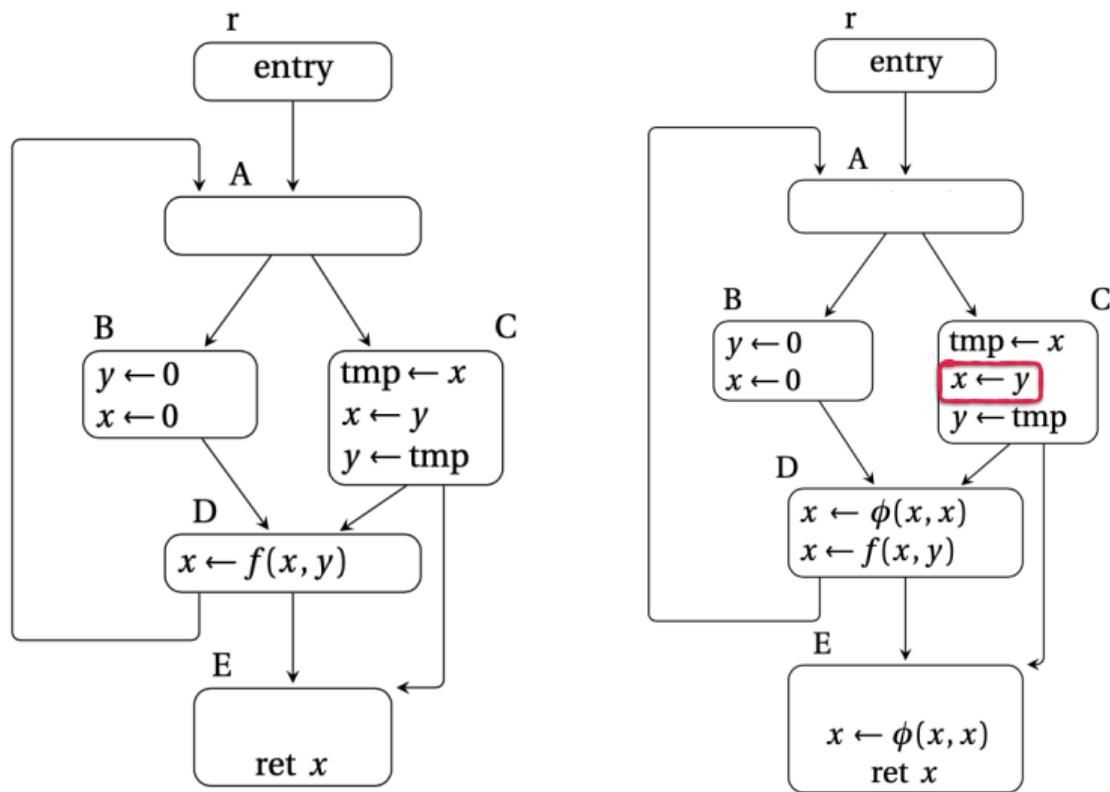
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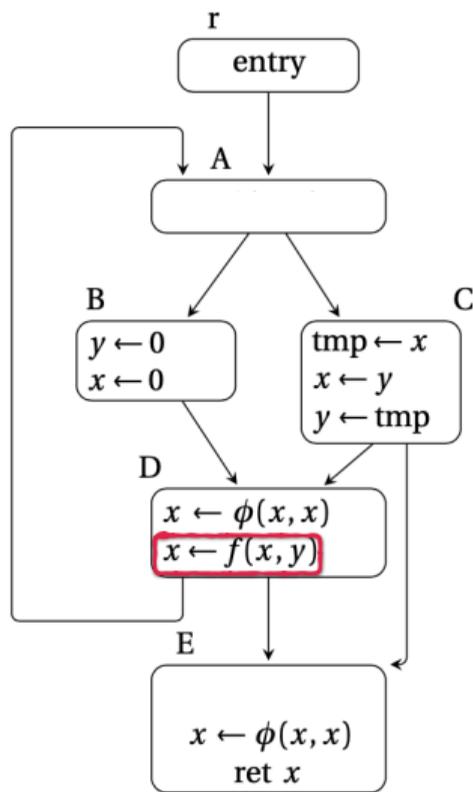
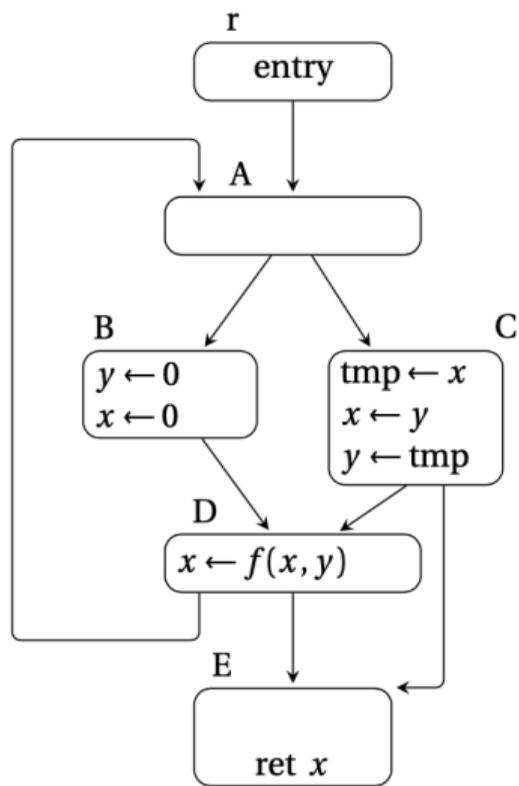
**Convince yourself it converges!**

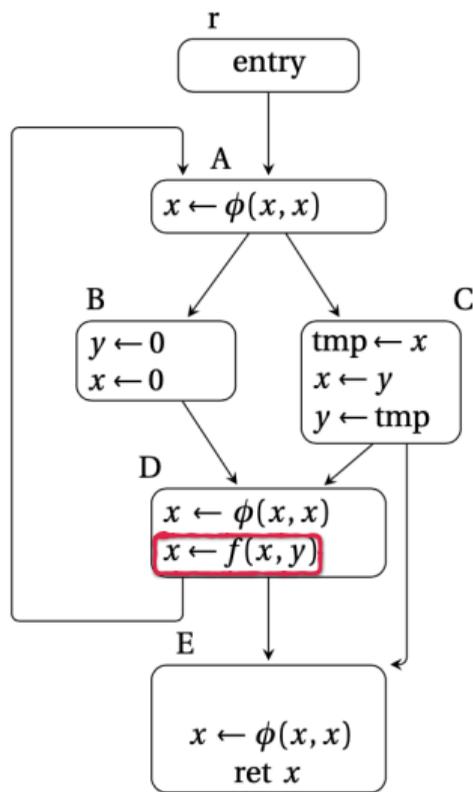
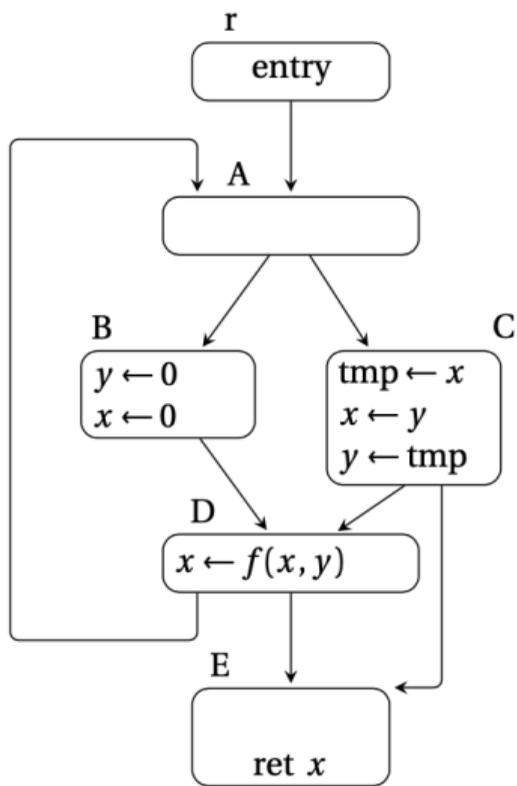


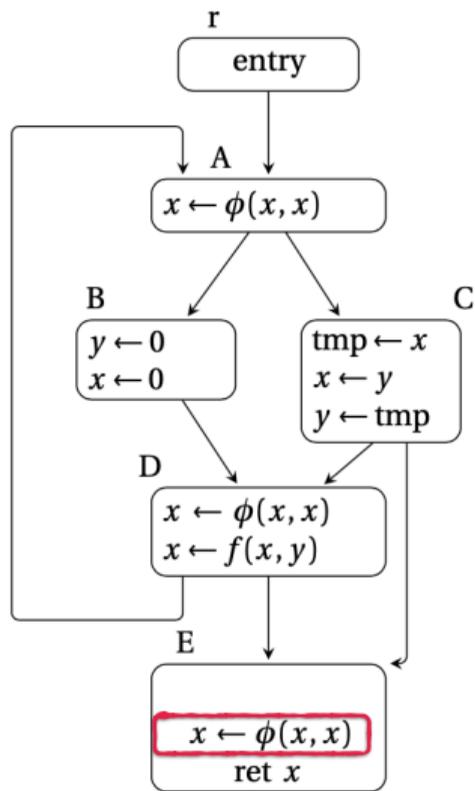
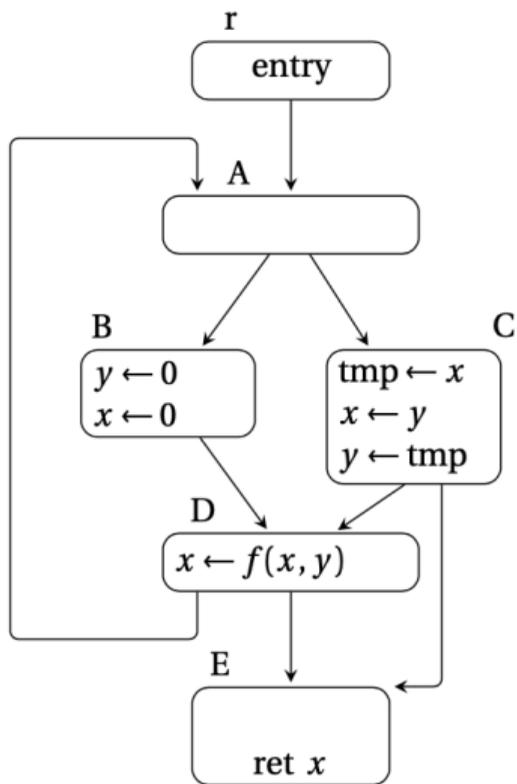


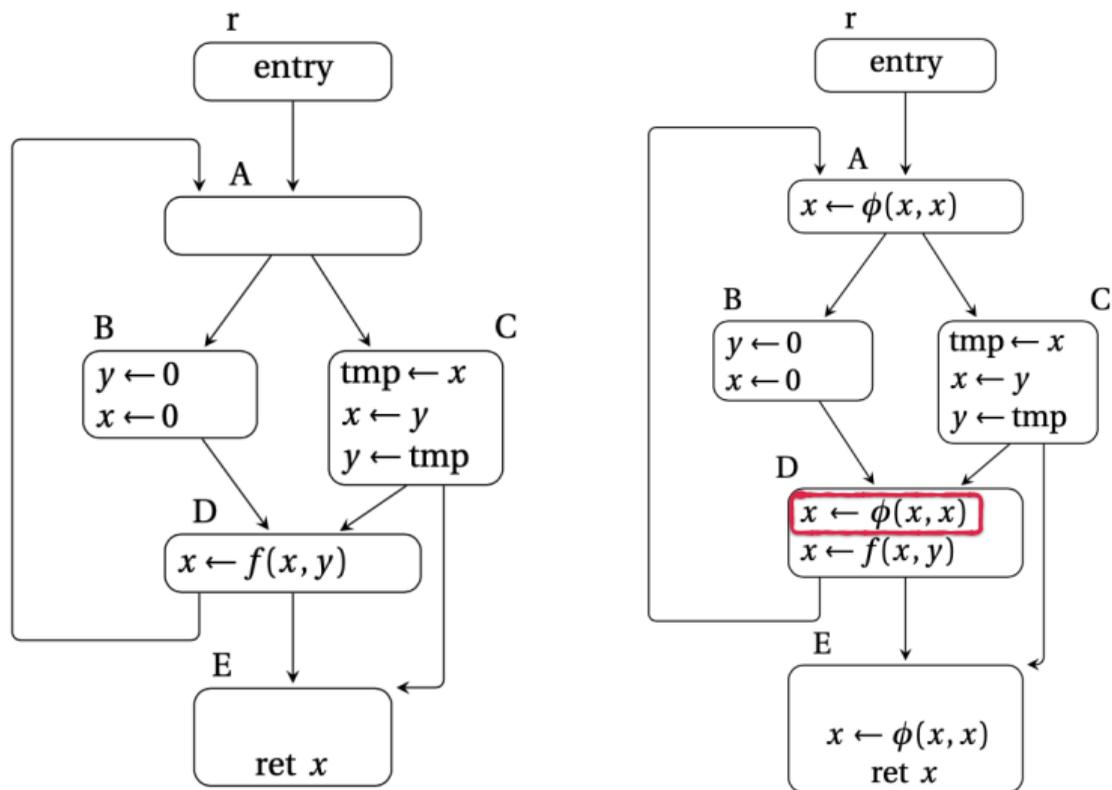


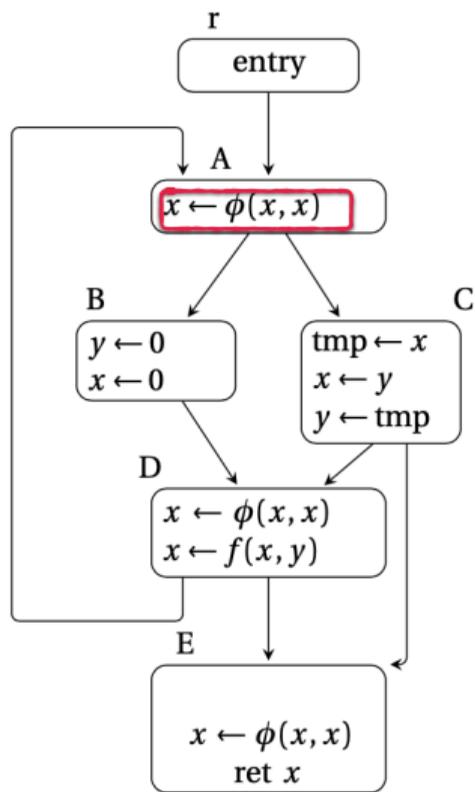
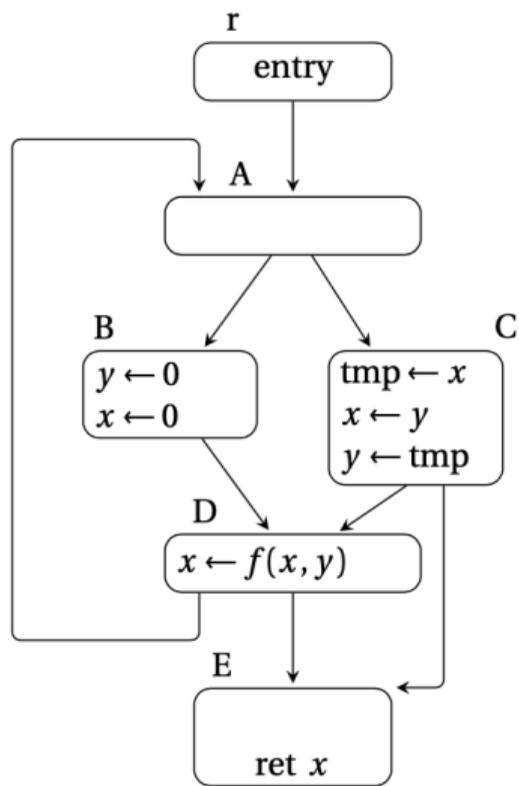


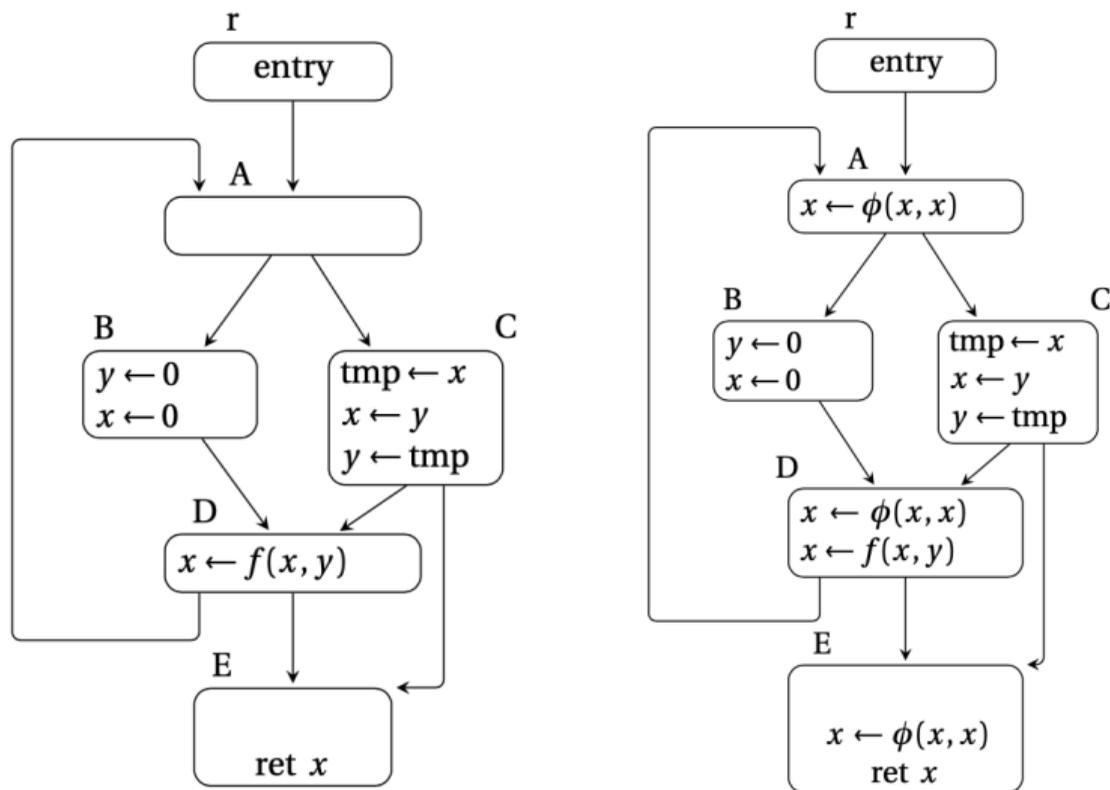


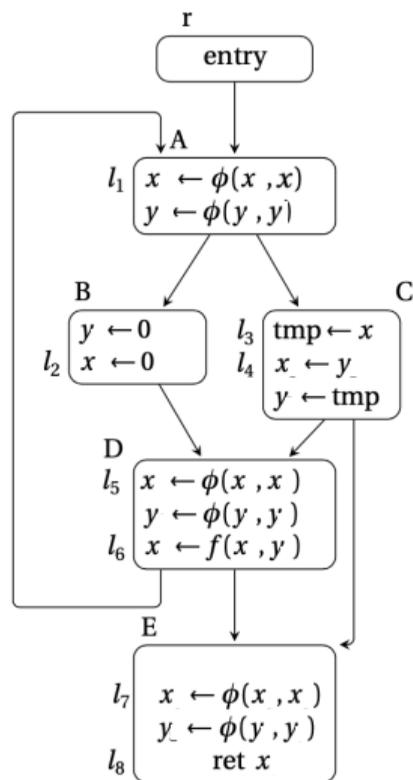
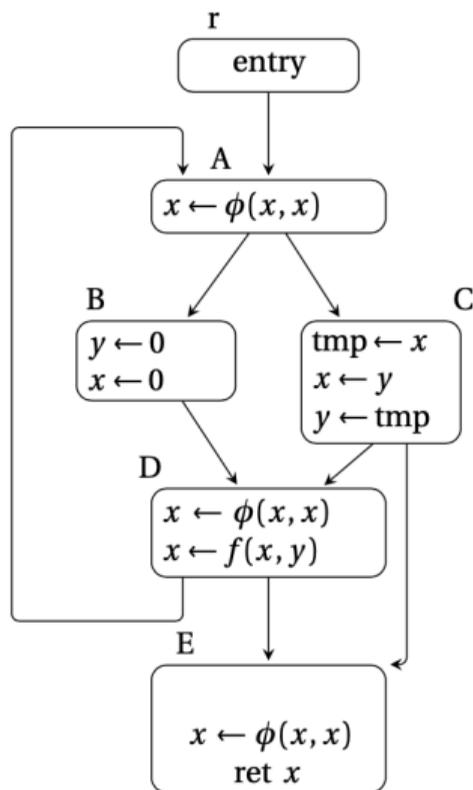
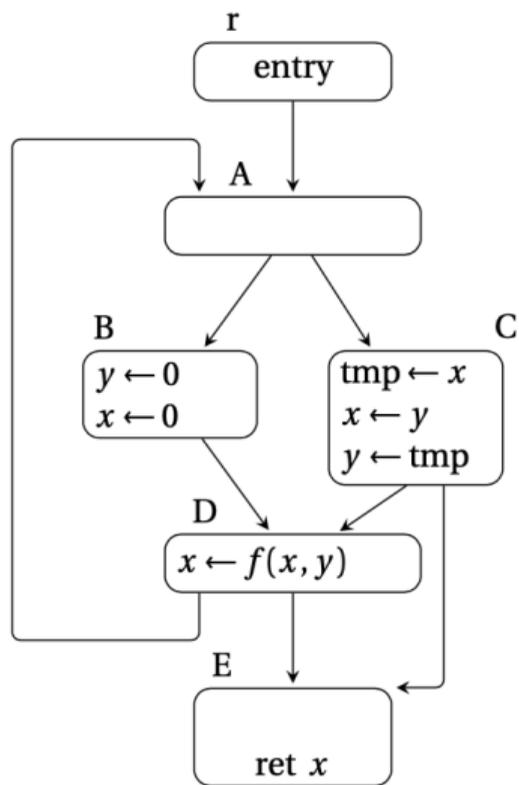












## Renaming variables

`stack[x]` : for each variable, we maintain a stack of names (“x\_i”)

`rename_aux(block) ::=`

`rename() ::= rename_aux(entry)`

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`rename_aux(b)`

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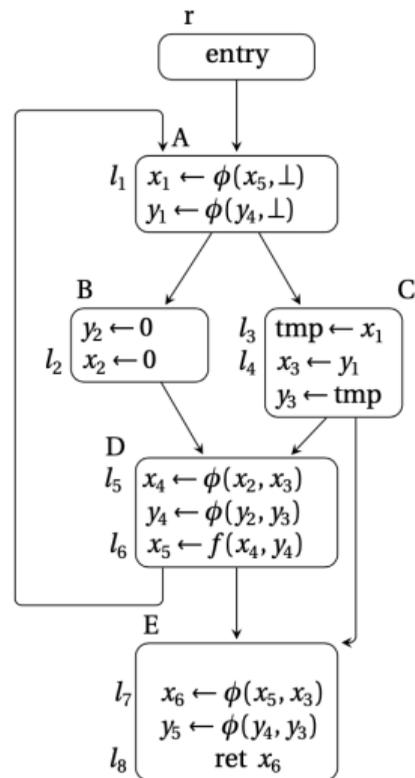
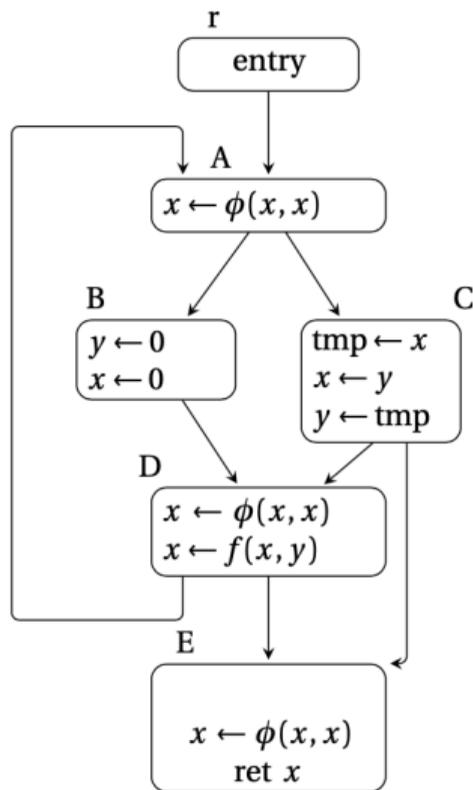
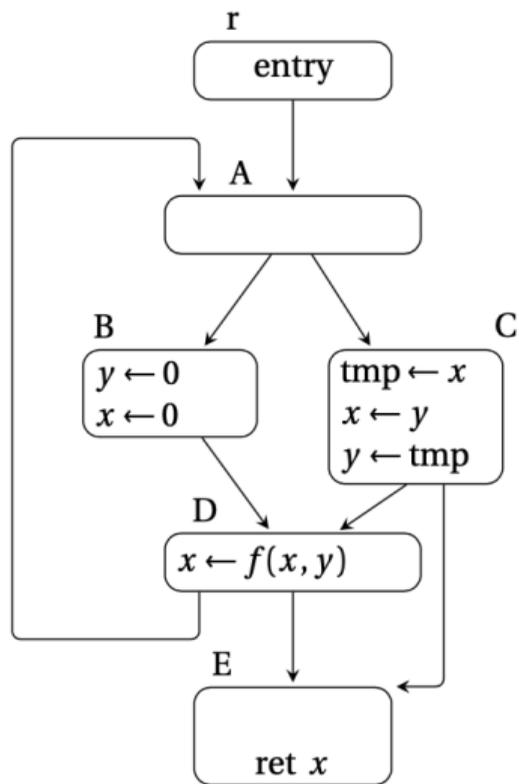
if `x` is read coming from `block`, replace `x` with `stack[x]`

for each successor `b` of `block` in the DT:

`rename_aux(b)`

pop from `stack` all variables introduced in this function call

`rename()` ::= `rename_aux(entry)`



## Converting out of SSA form

# From SSA to machine code

Processors do not support  $\Phi$ -nodes, we need to compile them away!

# From SSA to machine code

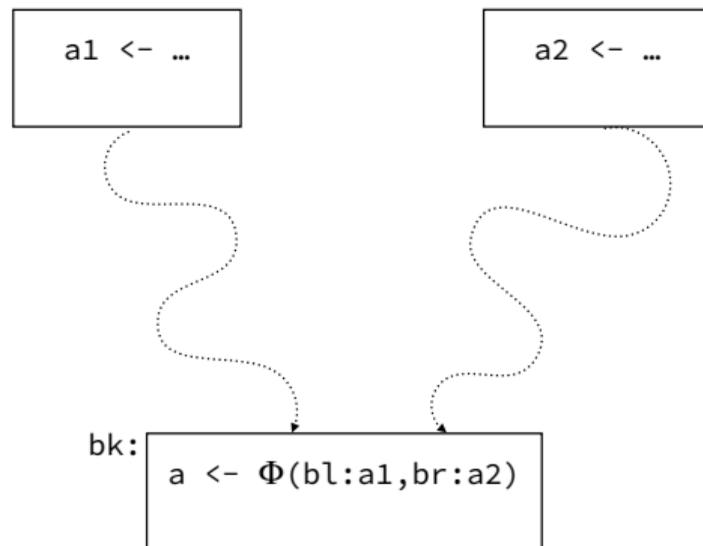
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bk: 

<code>a &lt;- <math>\Phi</math>(bl:a1,br:a2)</code>
---

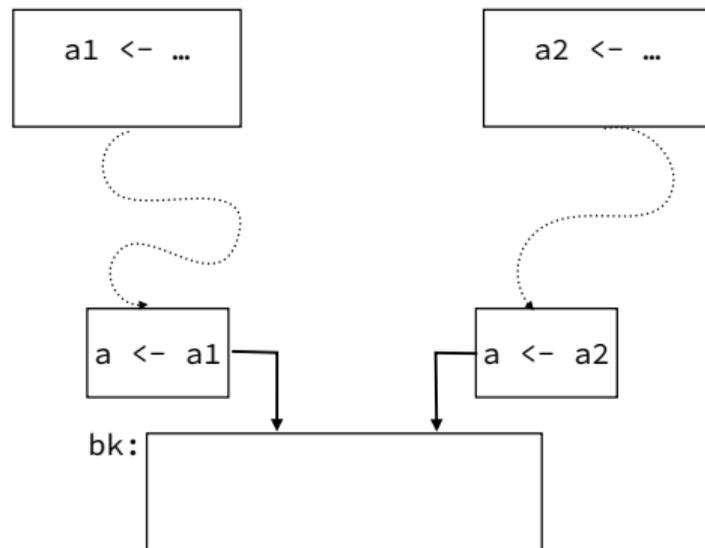
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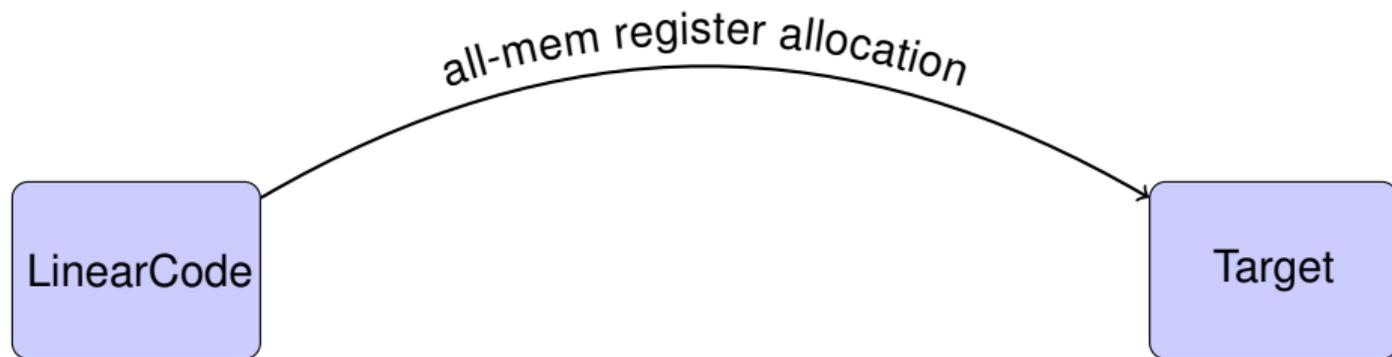


A good register allocator should then take care of eliminating needlessly introduced mov

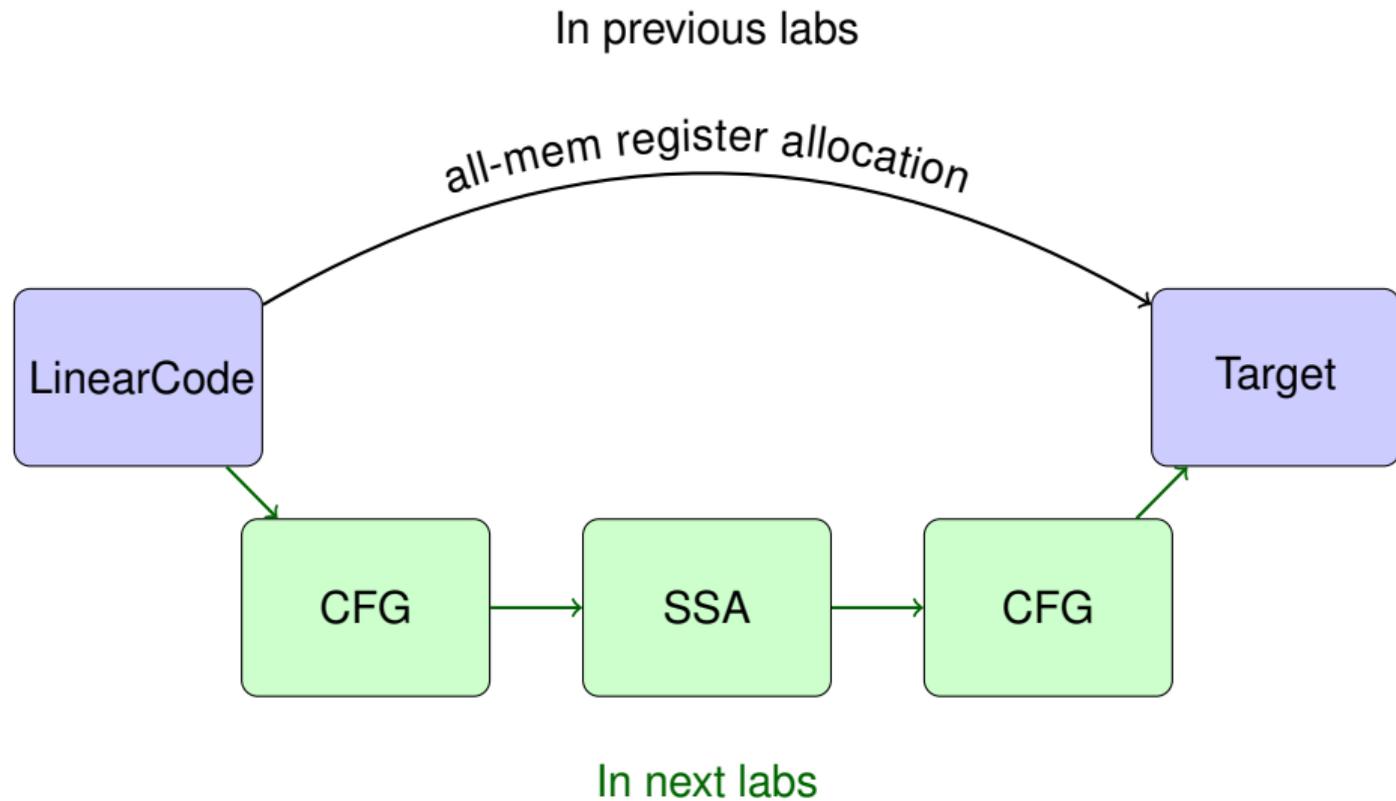
- 1 SSA Control Flow Graph
- 2 LAB: CFG + SSA
- 3 Exercises

# Code Generation

In previous labs



# Code Generation



# Steps

- 1 Implement Leader algorithm (from Linear code to CFG)
- 2 Implement SSA entry (dominance frontier and  $\phi$ -insertion)
- 3 Implement SSA exit

- 1 SSA Control Flow Graph
- 2 LAB: CFG + SSA
- 3 Exercises

## To SSA and back again

```
i=1; j=1; k=0;
while (k < 100) {
  if (j < 20) {
    j=i;
    k=k+1;
  } {
    j=k;
    k=k+2;
  }
}
return j;
```

(Exercise taken from Fernando Pereira)

- 1 Draw the CFG
- 2 Compute the Dominance Tree and the Frontier
- 3 Convert to SSA
- 4 Convert out of SSA

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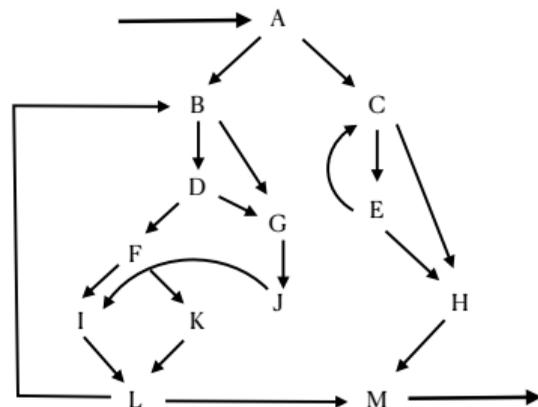
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```

# Testing dominance in constant time

We wish to test in constant time whether a given node dominates another. We assume that we have already computed the dominance tree, and allow ourselves to this end a little pre-processing.

Q1. Draw the dominance tree of the graph on the right



Q2. Write an instrumented depth-first traversal labeling each node of the dominance tree with two numbers:

- N: the order in which that node was visited
- A: the maximum N among the node's descendants

Q3. Prove that these annotations can be used to test dominance in constant time.

# Summary

- 1 SSA Control Flow Graph
- 2 LAB: CFG + SSA
- 3 Exercises